COMPILED: LEL CHECKED: FL APPD: SHEET 1 OF 12			TM	C SPEC	CIFICAT	ION		NO. S	90	2	
TOTAL AND THE	REV: OA	B							\top	ĪT	
	COMPILED:	LEL		CHECKED:	#EL	APPD:	AMbay 11	SHEET	. <u> </u>	OF 1	
	TITLE:						76/1	45	1	1	. <u>Z</u>

FINAL TEST PROCEDURE

for

DDR-5K SYSTEM and

DDR-5KA SYSTEM

TM	IC SPECIFICATION)N	NO. 5 9	82
REV: DAB				
COMPILED: LEL	CHECKED:	APPD:	SHEET	2 OF 12
TITLE: FINAL TEST PR	ROCEDURE - DDR-5K SYSTEM	AND DDR-5KA SYSTEM		

A. PRELIMINARY

Typed by mtp 6/29/65

1. Test Equipment Required

- a. Frequency Counter HP 524C or equivalent.
- b. Signal Generator (1) Measurements Corp. Model 82 or equivalent.
- c. AC VTVM Ballantine 314 or equivalent.
- d. 50 ohm, 20 db attenuator pad.
- e. Spectrum analyzer PTE-4 or equivalent.
- f. VOM-Simpson 260 or equivalent.
- g. SONIC ANALYZER LP-1A or equivalent.
- 2. It is assumed that individual units have been aligned and tested, that all units are installed in the cabinet, and that all inter-connections have be n made.
 - 3. Connect the power cable to 117V, 60 cps single phase source.
 - 4. Place all switches and controls in the following positions:

* a. HFP-1:

STANDBY-OFF Toggle Switch at rear to STANDBY. The main blower should commence to run. the GREEN STANDBY indicator should light. OVEN indicator on MCG-1 unit should commence to cycle.

b. MSA-1:

- (1) STANDBY-ON switch to STANDBY.
- (2) AGC DECAY controls 1/4 turn CW.
- (3) SQUELCH controls fully CCW.
- (4) MONITOR LEVEL control fully CCW.
- (5) MONITOR SELECT switch A₁.
 (6) LINE LEVEL controls 1/4 turn CW, except A₁ control which should be 1/2 turn CW.

c. MCG-1:

INT-AFC-SYN switch to SYN.

d. AFC-3:

- (1) SENSITIVITY control fully CW.
- (2) TUNING KCS control "0".
- (3) CARRIER SELECTOR switch OSC.

* RECORD ON TEST DATA SHEET

TN	NO. 5 982		
REV: OAB	AC SPECIFICATION		
COMPILED: LEL	CHECKED:	APPD:	SHEET 3 OF 12
TITLE: FINAL TEST PR	ROCEDURE - DDR-5K SYSTEM	AND DDR-5KA SYSTEM	12
Typed by mtp 6/29/65			

A. PRELIMINARY - Cont'd

e. HFR-2:

- (1) BAND BAND 1 (2-3 mcs).
- (2) NOISE SILENCER-OFF-ALIGNMENT SIGNAL switch to ALIGNMENT SIGNAL.
- (3) TUNE-SYNC-OPERATE switch - SYNC.
- (4) TUNE control 2 mcs.
- f. HFS-1: Control setting insignificant at this time.

g. HSS-8:

- (1) (2) CHANNEL MONITOR - A
- AGC SEP.
- (3) VOLUME control 1/2 turn CW. The VOLUME control may be adjusted to any convenient listening level during the test.

B. CHECKOUT PROCEDURE

- * 1. On MSA-1, place STANDBY-ON-POWER switch to ON. The YELLOW time delay indicator should light, and the GREEN standby indicator should go out. After approximately 60 seconds, YELLOW time delay indicator should go out, and the RED operate indicator should come on. At the same time, the following should occur:
 - a. HFR-2 dial should be illuminated.
 - The NIXIE indicators on the HFS-1 should light.
 - The GREEN channel indicators on the MSA-1 unit should light.
- * 2. With a Simpson 260 VOM, measure the voltage at the convenience outlets on the HPP-1 panel. It should be 117V AC, $\pm 10\%$.
- * 3. Pull out HFP unit. Measure the d-c voltage at test points TP8001 and TP8002 with a VOM. It should be +200V, exactly. If necessary, adjust the appropriate potentiometer(s) located adjacent to the test point(s). Replace HFP-1 drawer.

* 4. Check of Synthesizer, HFO Circuits & Synthesizer Stability

- a. Remove the plug from J1313 on HFR-2. Connect the frequency counter to J1313.
 - b. Set the NIXIE selectors on HFS-1 to 02.0000.
 - RECORD ON TEST DATA SHEET

TM	IC SPECIFICATI	ON	NO. S	982	2
REV: OAB					
COMPILED: LEL	CHECKED:	APPD:	SHEET	4	OF 12
TITLE: FINAL TEST PR	OCEDURE - DDR-5K SYSTEM	AND DDR-5KA SYSTEM	- 		
Typed by mtp 6/29/65					

- c. Carefully move the TUNE control on RF head around 2.0 mcs until an audible zero beat is obtained. The SYNC light may chatter during this operation.
- d. Place TUNE-SYNC-OPERATE switch to OPERATE. The SYNC indicator should come on and remain on. Move the TUNE control on RF head so that the SYNCHRONIZE meter reads "O" center scale. The frequency counter should read 3.75 mc.
- e. Place the TUNE-SYNC-OPERATE switch on RF head to SYNC. Place the 100 KC selector on HFS-1 to Position 1. Carefully move the TUNE control on RF head until an audible zero beat at 2.1 mcs is obtained. Place the TUNE-SYNC-OPERATE switch on RF head to OPERATE. SYNC light should come on. The frequency counter should read 3.85 mc.
- f. Continue this procedure for the remaining positions of the 100 KC selector as shown in the following table. Move the NIXIE selector to the position shown. With the TUNE-SYNC-OPERATE switch at SYNC, move the TUNE control until a zero beat is obtained, then throw the switch to OPERATE, and note that the SYNC indicator comes on. Read the counter.

100 KC SELECTOR	TUNE CONTROL	FREQ. COUNTER
2	2.2 mcs	3 05
3	2.3 mcs	3.95 mcs 4.05 mcs
14	2.4 mcs	4.15 mcs
5	2.5 mcs	4.25 mcs
6	2.6 mcs	4.35 mcs
7	2.7 mcs	4.45 mcs
8	2.8 mcs	4.55 mcs
9	2.9 mcs	4.65 mcs

g. Place the 10 KC NIXIE selector to Position 1. Repeat the foregoing procedure with the TUNE control at 2.91 mcs. The counter should read 4.66 mcs. Continue this procedure through the remaining positions of the 10 KC selector switch, in accordance with the following table:

10 KC SELECTOR	TUNE CONTROL	FREQ. COUNTER
2	2.92 mcs	4.67 mcs
3	2.93 mcs	4.68 mcs
4	2.94 mcs	4.69 mcs
5	2.95 mcs	4.70 mcs
6	2.96 mcs	4.71 mcs
7	2.97 mcs	4.72 mcs
8	2.98 mcs	4.73 mcs
9	2.99 mcs	4.74 mcs

TMC FORM SPEC 1

114 0 04 413

TM	C SPECIFICATION	ON	NO. S	982
REV: 0 A 8				
COMPILED: LEL	CHECKED:	APPD:	SHEET	5 OF 12
TITLE: FINAL TEST PROC	CEDURE - DDR-5K SYSTEM	AND DDR-5KA SYSTEM	_ 	
Typed by mtp 6/29/69)			

h. Place the 1 KC NIXIE selector to Position 1. Repeat the foregoing procedure with the TUNE control at 2.991 mcs. The counter should read 4.71 mcs. Continue this procedure through the remaining positions on the 1 KC selector switch in accordance with the following table:

1 KC SELECTOR	TUNE CONTROL	FREQ. COUNTER
2	2.992 mcs	4.742 mcs
3	2.993 mcs	4.743 mcs
4	2.994 mcs	4.744 mcs
5	2.995 mcs	4.745 mcs
6	2.996 mcs	4.746 mcs
7	2.997 mcs	4.747 mcs
8	2.998 mcs	4.748 mcs
9	2.999 mcs	4.749 mcs

i. Place the .1 KC NIXIE selector to Position 1. Repeat the foregoing procedure with the TUNE controls at 2.9991 mcs. The counter should read 4.7491 mcs. Continue this procedure through the remaining positions of the .1 kc selector switch in accordance with the following table:

.1 KC SELECTOR	TUNE CONTROL	FREQ. COUNTER
2	2.9992 mcs	4.7492 mcs
3	2.9993 mcs	4.7493 mcs
4	2.9994 mcs	4.7494 mcs
5	2.9995 mcs	4.7495 mcs
6	2.9996 mcs	4.7496 mcs
7	2.9997 mcs	4.7497 mcs
8	2.9998 mcs	4.7498 mcs
9	2.9999 mcs	4.7499 mcs

- j. Place the NIXIE selectors to 03.0000. Repeat the synchronization procedure at this frequency. The counter should read $4.75~{\rm mcs}$.
- k. Move the BAND control on RF head to Band #2, (3-4 mcs). Repeat the synchronization procedure at 3 mcs. The counter should read 4.75 mcs.
- 1. Continue this procedure for the remaining positions of the MC NIXIE selector switch, conducting the check at the high and low ends of each band. In each case, the counter should read 1.75 mc above the selected frequency.

MC SELECTOR	BAND	TUNE CONTROL	FREQ. COUNTER
4 5 6 6	2 3 3 3 4	4 mcs 4 mcs 5 mcs 6 mcs 6 mcs	5.75 mes 5.75 mes 6.75 mes 7.75 mes 7.75 mes

TI	MC SPECIFICA	ATION				NO. S	982		
REV: DAB									
COMPILED: LEL	CHECKED:	API	PD:			SHEET	6	OF	12
TITLE: FINAL TEST F	PROCEDURE - DDR-5K S	YSTEM AN	D DDR-5K	A SYS	ΈM				
Typed by mtp 6/29/	65								

MC SELECTOR	BAND	TUNE CONTROL	FREQ. COUNTER
7	4	7 mcs	8.75 mcs
8 8	14	8 mcs	9.75 mcs
	5	8 mcs	9.75 mcs
9	5 5 5 5 6	9 mcs	10.75 mcs
10	5	10 mcs	11.75 mcs
11	5	ll mcs	12.75 mcs
12	5	12 mcs	13.75 mcs
12		12 mcs	13.75 mcs
13	6	13 mcs	14.75 mcs
14	6	14 mcs	15.75 mcs
15	6	15 mcs	16.75 mcs
16	6	16 mcs	17.75 mcs
16	7	16 mcs	17.75 mcs
17	7	17 mcs	18.75 mcs
18	7	18 mcs	19.75 mcs
19	7	19 mcs	20.75 mcs
20	7	20 mcs	21.75 mcs
21	7	21 mcs	22. 75 mcs
22	7	22 mcs	23.75 mcs
23	7	23 mcs	24.75 mcs
24	7	24 mcs	25.75 mcs
24	8	24 mcs	25.75 mcs
25	8	25 mcs	26.75 mcs
26	8	26 mcs	27.75 mcs
27	8 8	27 mcs	28.75 mcs
28		28 mcs	29.75 mcs
29	8	29 mcs	30.75 mcs
30	8	30 mcs	31.75 mcs
31	8	31 mcs	32.75 mcs

m. Set the NIXIE selectors to 31.9999, and conduct the synchroniza tion procedure. The counter should read 33.7499 mcs.

 $\,$ n. Remove the counter from J1313 on RF head, and replace the 50 ohm termination.

* 5. Check of AFC-3:

- a. Synchronize the RF head at 02.0000 mcs. Leave NOISE SILENCER-OFF-ALIGNMENT SIGNAL switch on ALIGNMENT SIGNAL.
 - b. On MCG-1, place INT-AFC-SYNTH switch to AFC.

TM	NO. 5 982		
REV: OAB			
COMPILED: LEL	CHECKED:	APPD:	SHEET 7 OF 12
TITLE: FINAL TEST PRO	OCEDURE - DDR-5K SYSTEM	AND DDR-5KA SYSTEM	
Typed by mtp 6/29/69			

- c. On AFC-3, depress and hold down the RED RESET button, and carefully adjust the TUNING KCS control until the following occur simultaneously:
- (1) Maximum level is noted in the GREEN scale on the CARRIER LEVEL meter.
 - (2) The FADE indicator is extinguished.
 - (3) The DRIFT meter reads "0" center scale.
- (4) The DRIFT ALARM lamp is extinguished. When the proper conditions are obtained, release the RESET button. The conditions noted above should remain, and the DRIFT METER indicator should be steady at "0" center scale.
- d. On AFC-3, place CARRIER SELECTOR switch to RCC. The indications noted in "c" above should be unchanged. Return the CARRIER SELECTOR switch to OSC position.
- e. On HFR-2, place the NOISE SILENCER-OFF-ALIGNMENT SIGNAL switch to OFF, and the following conditions should appear on AFC-3:
 - (1) The CARRIER LEVEL indication should fall.
 - (2) The FADE ALARM should light.

After a wait of approximately ten seconds, return the NOISE-SILENCER-OFF-ALIGNMENT SIGNAL switch on RF head to ALIGNMENT SIGNAL. The conditions of Para. 5 "c", (1) through (4), should be restored on the AFC-3 unit without adjustment of controls.

f. On AFC-3, turn the SENSITIVITY control fully CCW. The FADE indicator should light and the CARRIER LEVEL indication should fall. Return the SENSITIVITY control fully CW.

NOTE: A FURTHER TEST OF THE AFC-3 WILL BE MADE IN CONJUNCTION WITH THE MSA-1.

6. Check of MSA-1 (in conjunction with AFC-3 & HSS-8)

- a. Ascertain that the reciever is still synchronized at 2.0000 mcs.
- b. On HFR-2, place NOISE SILENCER-OFF-ALIGNMENT SIGNAL to NOISE SILENCER.
 - c. On MSA-1, rotate each SQUELCH control fully CCW.
- d. On MSA-1, manipulate each LINE LEVEL control until the appropriate LINE LEVEL meter reads approximately -10VU.

TMC FORM SPEC 1

1M 0 44 A1A

TM	IC SPECIFICATION	ON	NO. S	982	
REV: DAB				T I I	
COMPILED: LEL	CHECKED:	APPD:	SHEET	8 0	F 12
TITLE: FINAL TEST PR	OCEDURE - DDR-5K SYSTEM	AND DDR-5KA SYSTEM			
Typed by mtp 6/29/65					

- e. On MSA-1, move each SQUELCH control slowly CW, until the appropriate channel just cuts off, as indicated by the extinction of the GREEN channel light.
- f. On HFR-2, place NOISE SILENCER-OFF-ALIGNMENT SIGNAL switch to ALIGNMENT SIGNAL.
- g. With the INT-AFC-SYN switch on MCG-1 still in AFC position, hold down the RESET button on AFC-3 and move TUNING KCS control to the 3KC position. Release RESET button. Channel B₂ LINE LEVEL meter should indicate, and the B₂ GREEN channel indicator only should light.
- h. As the TUNING KCS control on AFC-3 is moved slowly CCW to the -3KC position, Channels B_1 , A_1 and A_2 should activate in turn, to the exclusion of the other channels. Leave the TUNING KCS control at -3 KC with Channel A_2 activated.
- i. Plug headphones into the MONITOR jack on MSA-1, and rotate the MONITOR LEVEL control fully CW. Put MONITOR SELECTOR switch in the $\rm A_2$ position.
- j. On HSS=8, place CHANNEL MONITOR to A_2 , and advance GAIN control CW for a convenient listening level. A tone should be heard in both phones and monitor speaker.
- k. Slowly move the TUNING KCS control on the AFC-3 CW toward +3KC. As Channels B_1 and B_2 activate, move the MONITOR SELECTOR switch on MSA-1 #1 and the CHANNEL MONITOR on the HSS-8 to the appropriate position, and listen for a tone in the phones and monitor speaker. Leave AFC TUNING KCS control at +3 KC.

* 7. Dynamic AGC Check

a. Set controls on Receiver as follows:

(1) HFR-2 -

BAND - BAND #1 (2-3 mcs)

TUNE - 2.5 mcs

NOISE SILENCER-OFF-ALIGNMENT SIGNAL - OFF TUNE-SYNC-OPERATE - SYNC

- (2) HFS-1 NIXIE SELECTORS TO 02.5000.
- (3) MCG-1 INT-AFC-SYN switch to SYN.
- (4) MSA-1 No adjustment.

*RECORD ON TEST DATA SHEET

TMC SPECIFICATION						NO. S	182			
REV:	AB								_]	
COMPILED:	LEL	CHECKED:		APPD:			SHEET	9	OF 12	
TITLE:	FINAL TEST P	PROCEDURE - DDF	R-5K SYSTE	M AND	DDR-5K	A SYSTEM				
Typed 1	by mtp 6/29/6	5								

* 7. Dynamic AGC Check

- b. Connect an RF signal generator to antenna jack on HFR-2 via a 20 db 50 ohm pad. Adjust signal generator for 100,000 uv at 2.5 mcs.
- c. Synchronize the RCVR system at 2.5 mcs, and lock the TUNE control on HFR-2.
 - d. Vernier tune the signal generator to activate Channel A_1 , MSA-1.
- e. Adjust Channel ${\rm A_1}$ LINE LEVEL control on MSA-1 until Channel ${\rm A_1}$ level meter reads 0 VU.
- f. Reduce signal generator output to 10 uv (on signal generator calibrated attenuator). The indication on Channel A_1 level meter, MSA-1, should not fall below -3 VU.
 - g. Repeat Para B7 at: 3.5; 5; 7; 10; 14; 20 and 28 mcs.

8. Signal Plus Noise/Noise Check (Sensitivity)

a. Receiver:

- (1) Synchronize the RCVR system at 2.5 mcs. Lock the TUNE control on HFR-2, and leave the TUNE-SYNC-OPERATE switch at OPERATE.
- (2) Connect a Ballantine Model 314 VTVM (or equivalent) to terminals 7 and 8 (A $_1$ audio output) of terminal board E6501 at rear of MSA-1.
- (3) Advance all SQUELCH controls on MSA-1, except A_1 control, fully CW. Leave A_1 SQUELCH control fully CCW.
- (4) Connect an RF signal generator, at 2.5 mcs, via a 20 db, 50 ohm pad, to the antenna input on HFR-2. Set signal generator frequency at 2.5 mcs, output to minimum.
- (5) On MSA-1, adjust Channel A_1 LINE LEVEL control for a reading of -20 VU on the A_1 LINE LEVEL meter. Note the DB indication on the Ballantine meter at this time.
- (6) On HSS-8, place CHANNEL MONITOR to A_1 , and advance the GAIN control for a convenient listening level.
- (7) Adjust signal generator output to 1000 uv, and vernier tune the signal generator to obtain approximately a 1 KC tone in Channel A.

RECORD ON TEST DATA SHEET

	TM	NO. 5 982							
REV: 0	AB								
COMPULED:	LEL	CHECKED:	APPD:	SHEET 10 OF	12				
TITLE:	TITLE: FINAL TEST PROCEDURE - DDR-5K SYSTEM AND DDR-5KA SYSTEM								
Typed	by mtp 6/29/6	55							

8. Signal Plus Noise/Noise Check (Sensitivity)

a. Receiver:

- (8) Adjust signal generator output until Ballantine meter reads 15 db above the reading obtained in Step (5). Read the signal generator output, and divide by 10. The reading must be 1 uv or less.
- (9) Repeat the signal plus noise/noise check as required on Test Data Sheet.

9. AF Distortion/Hum Level/Intermodulation (Adjacent Channel)

a. AF Distortion:

- (1) Connect TTG RF tones through attenuator to J6501 on MSA. Set attenuator for 60 db in.
 - NOTE: The TTG should have such RF crystals as to produce a 1KC separation between tones; e.g. 2000KC and 2001KC.
- (2) Set up signal generator to 1.75MC with 1V output. Connect to J6502 of MSA-1.
- (3) Connect Sonic Analyzer LP-1A to Channel B2 on terminal box, (gnd to cabinet/input to 600 ohm load).
- (4) Vary generator for indication on B2 VU meter. Set LINE LEVEL for 0 VU with one RF tone.
 - (5) Slowly vary generator to center two tones on analyzer.
 - (6) Set tones on analyzer for zero db reference.
 - (7) Increase tones 20 db. AF distortion should be 40 db or better.
 - (8) Leave equipment connected. DO NOT CHANGE ANY SETTINGS.

b. Hum Level:

- (1) With equipment and settings the same as in previous steps turn off the RF tones on the TTG.
 - (2) Hum Level on analyzer should be down 50 db or better.
 - (3) Turn on RF tones. DO NOT REMOVE EQUIPMENT.

c. Intermodulation (Adjacent Channel):

- (1) Vary signal generator to obtain an indication on the adjacent channels VU meter. Set for O VU.
 - (2) Any tones on analyzer must be down 60 db or better.
 - (3) Leave all test equipment connected.
 - (4) Repeat parts D-1,2, and 3 for Channels B1, A1 and A2.
- (5) Upon completion of Step d, disconnect all test equipment and re-connect all cables to proper jacks.

MPILED: LEL	CHECKED:	APPD:		SHEET	11 or 12
TLE: FINAL TEST F	PROCEDURE - DDR-5K SY	STEM AND DDR-5KA	SYSTEM		
Typed by mtp 6/29/	['] 65				
		CAL MATERIEL COR	Ρ.		
	MAMAR	ONECK, N.Y.			
	שביים ה	ATA SHEET #1			
	11101 01	AIR OHDET #1			

ERIAL NO.:	·	-	
A-4 a		rate DBY indicator indicators	
B-1		e delay indicator e indicator	
B-2	Conv. outl	et voltage	**************************************
B-3	Voltage at	TP8001 & 8002	
B-14	SYNTH, HFO	, STABILITY	***************************************
B - 5	Check of A	FC-3	
B - 6	Check of M	SA-1	
B-7 & B-8	AGC a	nd SENSITIVITY ss) (luv or less)	
	FREQ.	AGC CHECK	SENSITIVI
	2.5 mcs		***
	3.5 mcs	****	·
	5 mcs	·	•
	7 mcs	**************************************	
	10 mcs		
	14 mcs		
	20 mcs		
	28 mcs	**************************************	
DATE:			

TMC FORM SPEC 1

1M 8 64-AINS

7	MC SPECIFI	CATIO)N			NO. S		0.00	- · · · · ·	· · · · · ·	
REV: DAB								_982	T		Γ
COMPILED: LEL	CHECKED:		APPD:			SHEE	T -		OF	 12	L
TITI E.	PROCEDURE - DDR-5K	SYSTEM A	AND DDR-5	KA SYS	TEM						
Typed by mtp 6	*		-								
	TEST D	OATA SHEI	ET #2								
B. 9(a)7	AF Distortion (down 40 DB or be	etter)			_DB						
B. 9(b)2	Hum Level (Down 50 DB or be	etter)	· · · · · · · · · · · · · · · · · · ·		_DB						
B. 9(c)	Intermodulation (down 60 DB or be		nt Channe	e1)							
	Channe1	A1	· // * · · · · · · · · · · · · · · · · ·	·- -	_db						
	Channel	В1		·	_db						
	Ch a nne1	A2		· · · · · · · · · · · · · · · · · · ·	_db						
	Channel	В2			_db						
DATE:											

TESTER:

_	SION	SHEET	<u></u>	THE TECHNICAL MATE	THE TECHNICAL MATERIEL CORP. MAMARONECK NEW YORK LIST NO.		Ø
DÁTE	REV.	SHEET	EMN #		DESCRIPTI		APP.
6/29/6	1	1 of		O=ORIGINAL RELEASE			
12/27/6	5 A	a11	15450	Revised per EMN			K.S.
7/28/6	1			Revised sheets 2,10,1	2 per EMN		Jr. Sh
							7
	<u> </u>						
			<u> </u>				
	<u> </u>						
	<u> </u>		<u> </u>				ļ
	 		 				<u> </u>
		+					-
	 	-					
		-	 	·			
	 		 				<u> </u>
			ļ	-			ļ
	ļ		ļ				
	-				*		-
· · · · · · · · · · · · · · · · · · ·							1
							<u> </u>
		1					
-							ļ
		٠,					
	<u> </u>						
	<u> </u>		<u> </u>				
	-						
	<u> </u>						
	<u> </u>		 				ļ
	 						
		1	1	į –		· · · · · · · · · · · · · · · · · · ·	1

1 -