

DATE 3-3-53
SH. 1 OF 1
COMPILED BY
G.T.O.

TMC SPECIFICATION NO. S-154

TITLE: TEST PROCEDURE AMD

JOB

APPROVED

[Signature]

1. Connect test cable to AMD.
2. Turn all potentiometers maximum clockwise.
3. Set Osc. to \approx 400 cycles.
4. Connect Osc. to BFO tone terminal.
5. Set signal to 60 mv. (.06)
6. Measure dc-voltage on resistor side of L101 (-34)
7. Connect Osc. to HFO tone terminal 60 mv. input.
8. Measure dc-voltage on resistor side of L102 (-26)
9. Set Osc. to BFO tone terminal and reduce level to 0, set VTVM on BFO dc out, record positive voltage +4v.
10. Increase level till lights go on record level .003-004.
11. Increase level till 0 volts dc out .032 (32 mv.)
12. Increase level till 3 volt dc out .1 volts.
13. Rotate BFO ADJ maximum counter clockwise note that relay opens. Return pot maximum clockwise.
14. Set Osc to HFO tone terminal reduce to 0 set VTVM to HFO DC out, record voltage +3.0.
15. Increase signal level input for 0 dc out .017.
16. Increase signal level input for -2.5 dc out .08 volts.
17. Rotate HFO ADJ maximum counter clockwise dc should return to figure found in step 14.
18. Rotate DC ADJ pot maximum counter clockwise DC voltage should fall to 0.
19. Return DC ADJ and HFO ADJ maximum clockwise.