

## APPLICATION

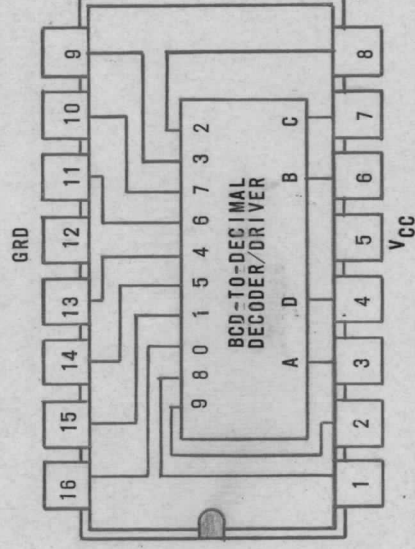
## REVISIONS

QTY	MODEL USED ON	ASSY NO.	LTR	DESCRIPTION	DATE	E.M.N.NO	DRAFT	CHKD	APPD
	HFRR-4		X	EXPERIMENTAL RELEASE	3/12/68		C.V.		<i>[Signature]</i>
			Ø	ORIG. RELEASE FOR PROD.	10-22-68	Ø	R.G.		<i>[Signature]</i>

MFGR (TMC CODE NO): S401-265  
 MFGR PART NUMBER: SN7441N  
 MFGR CATALOG: INTERGRATED CIRCUITS  
 1967-1968

RECOMMENDED OPERATING CONDITIONS  
 SUPPLY VOLTAGE  $V_{CC}$ : 4.75V TO 5.25V  
 MAX VOLTAGE AT ANY OUTPUT: 25V

POWER DISSIPATION: 95 mW  
 MAX VOLTAGE ON ANY OUTPUT: 55 V  
 (SUFFICIENT IF DRIVE GAS  
 FILLED READOUT TUBES DIRECTLY)



POSITIVE LOGIC

- PURCHASING NOTE:** VENDOR MUST INCLUDE THE FOLLOWING INFORMATION WHEN SHIPPING THIS ITEM TO TMC.
1. AN OUTLINE DRAWING OR ILLUSTRATION FROM CATALOG SHOWING ALL PERTINENT DIMENSIONS AND TOLERANCES.
  2. ELECTRICAL AND/OR MECHANICAL SPECIFICATIONS AS SHOWN IN THEIR CATALOGS.

REQ'D ITEM	PART NUMBER	DESCRIPTION	SYM.
LIST OF MATERIAL			
<b>THE TECHNICAL MATERIEL CORP.</b> MAMARONECK, NEW YORK			
NETWORK, BCD-TO-DECIMAL DECODER DRIVER			
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND INCLUDE CHEMICALLY APPLIED OR PLATED FINISHES			
DECIMALS .X ± .05 .XX ± .01 .XXX ± .005	FRACTIONS 1/64 ANGLES 0°-30'	DATE	
TOLS. 0°-30'		DATE	
MATERIAL			
FINISH			
HOGAN		DATE	
FINAL APPROVAL		DATE	
MECH. DES.		DATE	
ELECT. DES.		DATE	
CHECKED		DATE	
DRAWN		DATE	
SIZE <b>A</b>		CODE IDENT. NO. <b>82679</b>	ISSUE <b>Ø</b>
SCALE		DWG NO. <b>NW 165</b>	OF <b>SHEET</b>

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