

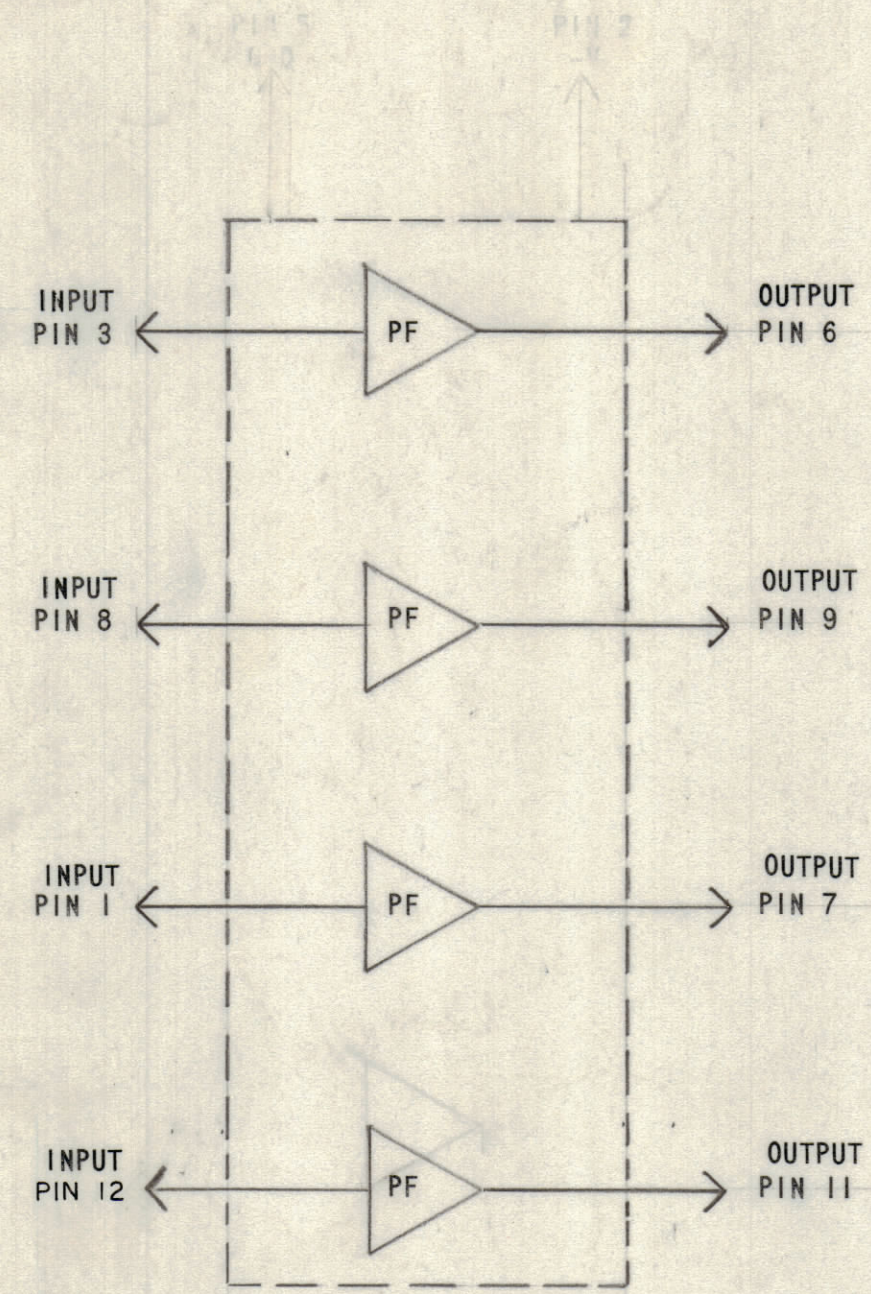
4

3

2

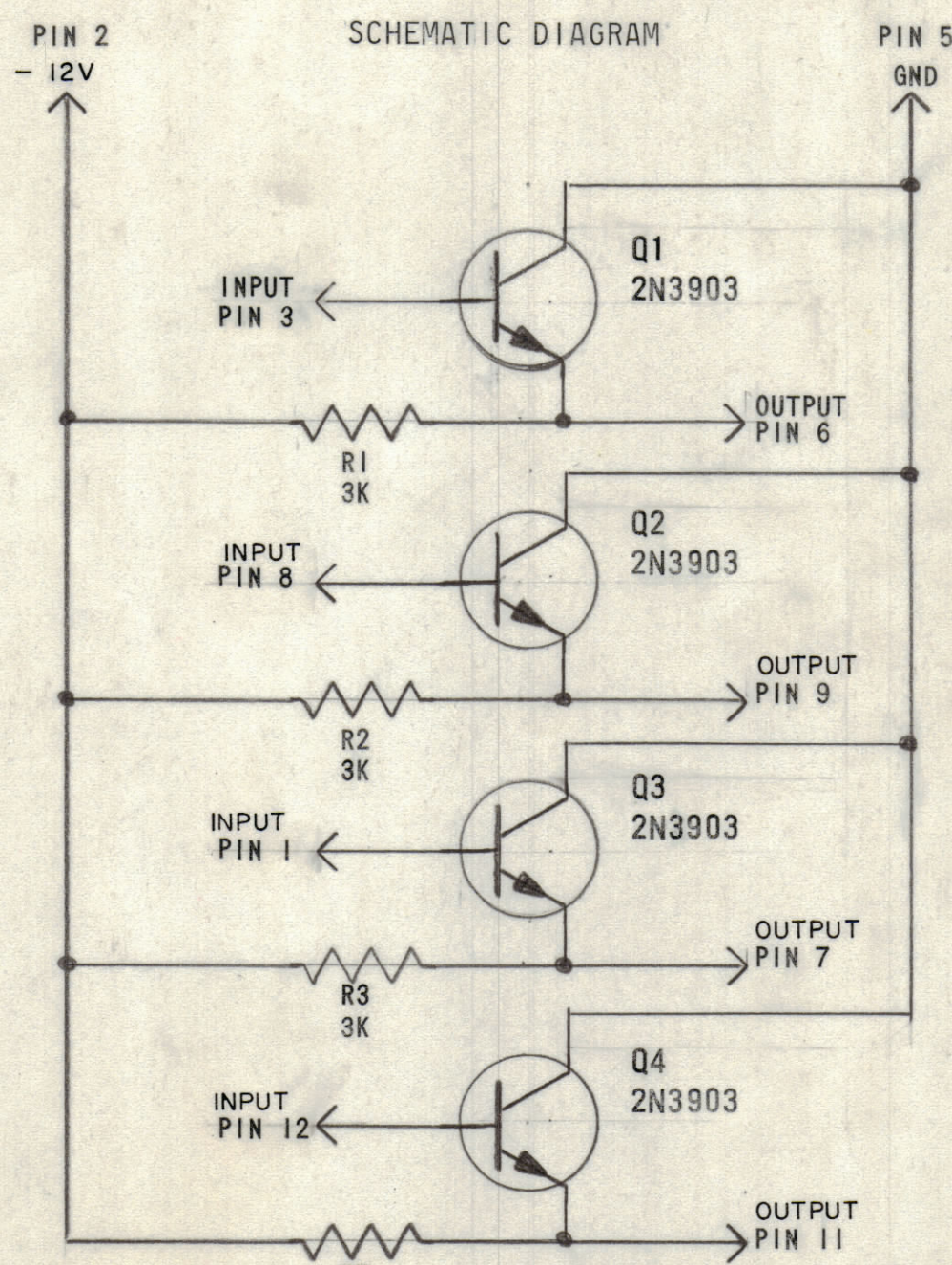
1

LOGIC DIAGRAM



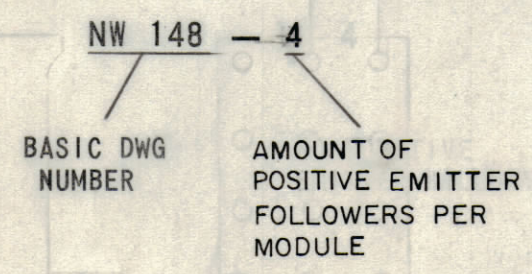
RESISTORS ARE IN OHMS, 1/4W 5%

SCHEMATIC DIAGRAM



RESISTORS ARE IN OHMS, 1/4 WATT, ±5%

TMC PART NUMBER SHALL APPEAR IN THE FOLLOWING FORM:



REVISIONS						
ZONE	LTR	DESCRIPTION	DATE	E.M.N.NO	DRAFT	CHKD APPD
	X	EXPERIMENTAL RELEASE	2/24/69		C.V.	
	X1	ELEC SPEC REVISED	1/10/69		KH	MG
	X2	12 VOLT LOGIC TO ELEC. SPEC. ADD	1/16/69		KH	MG
	Ø	ORIGINAL RELEASE FOR PRODUCTION	2/1/70		W	

ELECTRICAL SPECIFICATIONS :

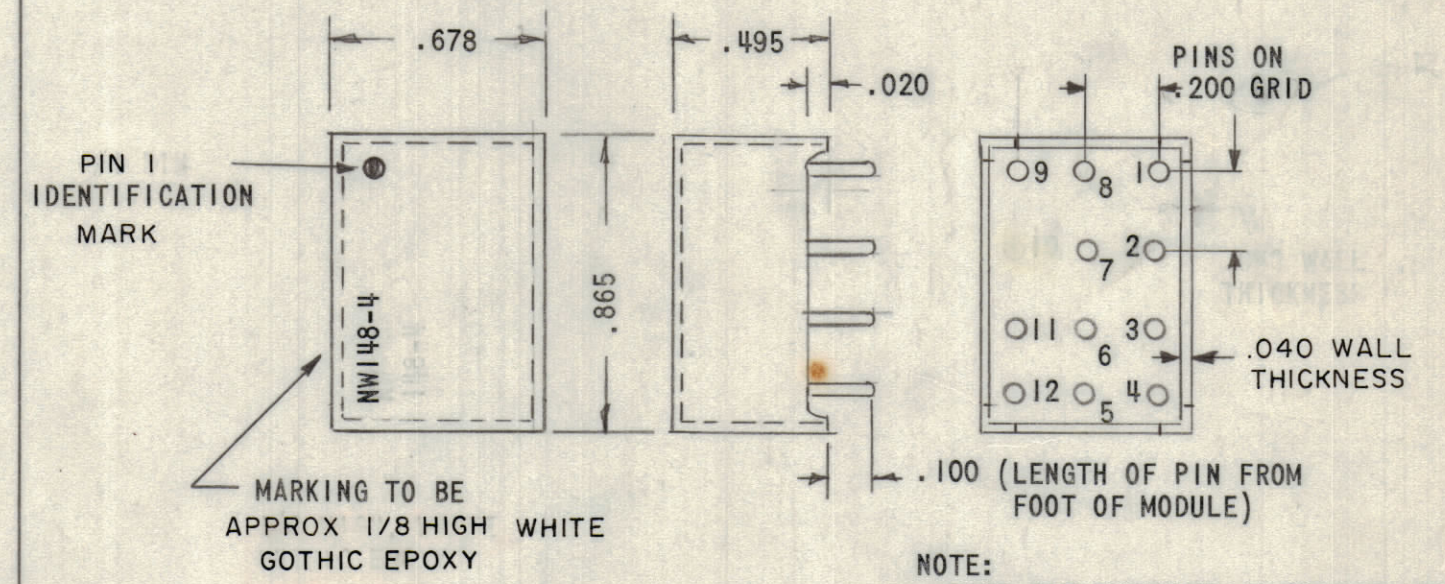
12 VOLT LOGIC
 PIN 5 GND
 PIN 2 -12V
 OPERATING TEMP 0°C TO+75°C.

SWITCHING CHARACTERISTICS

EACH INPUT MIN VOLTAGE - 5.3 VDC
 EACH OUTPUT MIN VOLTAGE -6.0 VDC

MECHANICAL SPECIFICATIONS:

POTTING COMPOUND: EPOTEC 509
 CASE MATERIAL: DIALLYL PHTHALATE
 POLARIZING PIN: PIN #10



NOTE:
 PINS ARE EXTRUDED COMPONENT LEADS
 (DO NOT USE TRANSISTOR LEADS FOR MOUNTING PINS)

QTY. REQ.	ITEM	PART NO.	DESCRIPTION	SYMBOL
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LIST OF MATERIAL

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND INCLUDE CHEMICALLY APPLIED OR PLATED FINISHES	FINAL APPROVAL <i>S. DeMarco</i> MECH. DES. <i>R. Kasper</i>	DATE 2/24/70
TOLERANCES ON	ELECT. DES.	DATE
DECIMALS .X ± .05 .XX ± .01 .XXX ± .005	CHECKED <i>J. Deierlein</i>	DATE 9/24/69
FRACTIONS ± 1/64 ANGLES ± 0° -30'	DRAWN <i>Deierlein</i>	DATE 9/22/69

THE TECHNICAL MATERIEL CORP.
 MAMARONECK, NEW YORK

NETWORK, DIGITAL
 POSITIVE EMITTER FOLLOWER

QTY / UNIT	DDRR-10M, 10K	MODEL USED ON	ASS'Y NO.
APPLICATION			
CODE			

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SIZE	CODE IDENT. NO.	DWG NO.	ISSUE
C	82679	NW 148	Ø
SCALE		SHEET	OF

4

3

2

1