



REVISIONS							
E.M.N.NO	DRAFT	CHKD	ZONE	LTR	DESCRIPTION	DATE	APPROVED
	1/1			X	EXP. RELEASE	12/15/69	
				X1	ADD PIN L, R17, CR14 + CR15	12-19-69	
				0	ORIGINAL RELEASE FOR PRODUCTION	1-5-70	

- UNLESS OTHERWISE SPECIFIED
1. ALL DIODES ARE IN914
  2. ALL RESISTANCE VALUES ARE IN OHMS, 1/4 W.
  3. ALL CAPACITANCE VALUES ARE IN  $\mu$ F FARADS.
  4. ALL INDUCTANCE VALUES ARE IN  $\mu$ L HENRIES.

NOTE:  
PARTIAL REFERENCE DESIGNATIONS SHOWN,  
FOR COMPLETE DESIGNATIONS PREFIX WITH  
UNIT NUMBER AND SUB-ASSEMBLY  
DESIGNATION(S) AS APPLICABLE.

MODULE VOLTAGE & GND CHART				LAST SYMBOL	MISSING SYMBOL
SYMBOL	+12V	-12V	GND		
Z1, 4, 3	10		5	C6	
Z2, 5		2		CR15	
Z6	10	2	5	L5	
Z7		2	5	Q5	
Z8	10	2, 9	4, 5	R17	
				TP13	
				Z8	

I	RTTD - 5A	A 4821
QTY / UNIT	MODEL USED ON	ASS'Y NO.
APPLICATION		
	CODE	

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QTY. REQ.		ITEM	PART NO.	DESCRIPTION	SYMBOL
LIST OF MATERIAL					
FINAL APPROVAL		DATE		<b>THE TECHNICAL MATERIEL CORP.</b> MAMARONECK, NEW YORK  <b>DIAGRAM SCHEMATIC</b> <b>LEDEX GATING CIRCUIT</b>	
MECH. DES.		DATE			
ELECT. DES.		DATE			
CHECKED		DATE		SIZE CODE IDENT NO. DWG NO. ISSUE <b>C 82679 CK 1686</b>	
DRAWN		DATE			
MATERIAL				SCALE <i>NONE</i> SHEET OF	
FINISH					