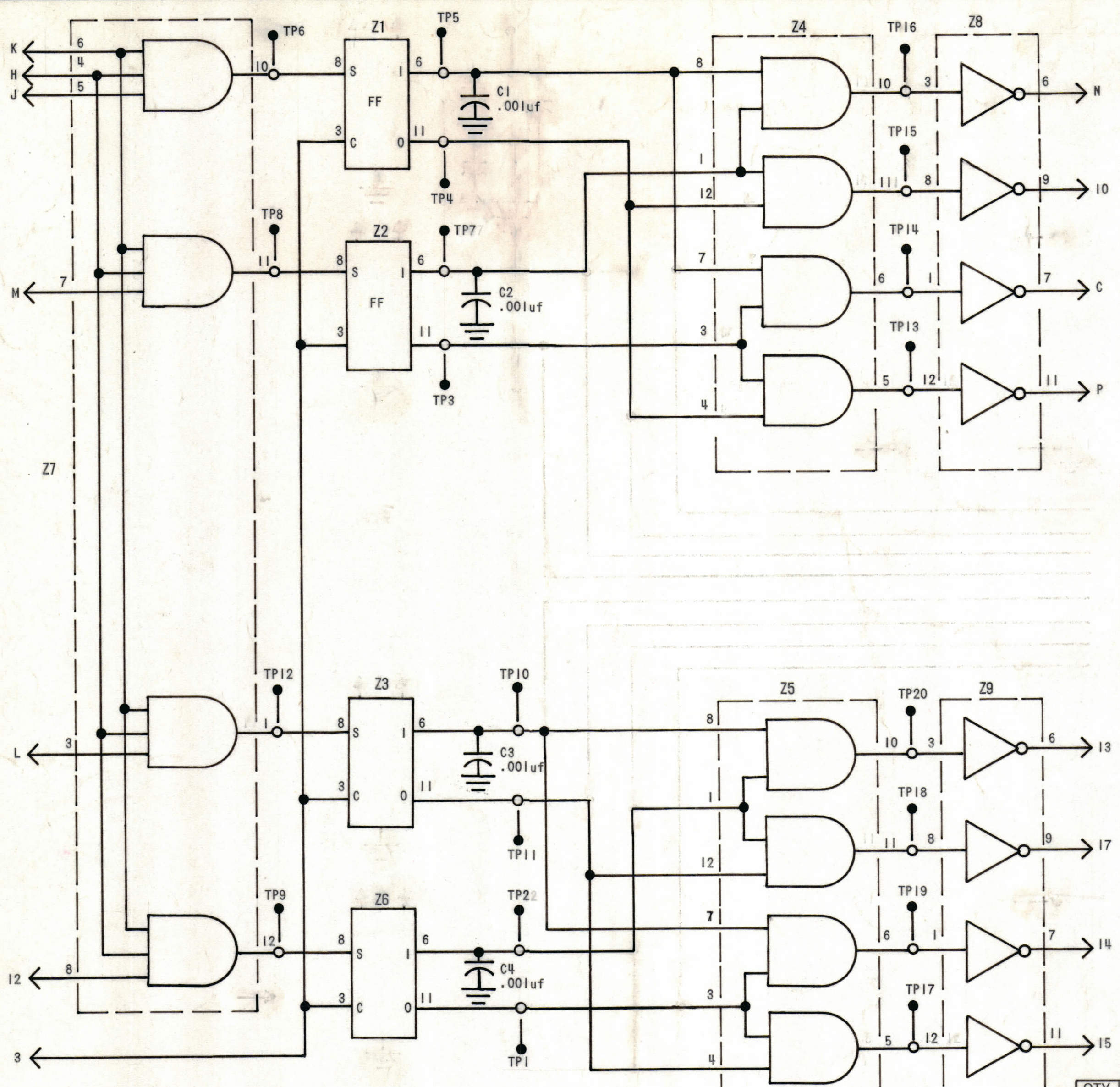


REVISIONS						
ZONE	LTR	DESCRIPTION	DATE	E.M.N.NO	DRAFT	CHKD
	X	EXP. RELEASE	6/19/69		GE	
	Ø	ORIGINAL RELEASE FOR PRODUCTION	8/2/69		GE	

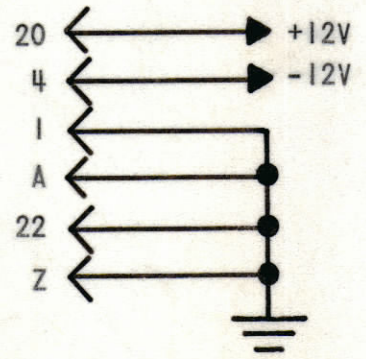


MODULE VOLTAGE & GND CHART			
SYMBOL	PIN CONNECTIONS		
	+12V	-12V	GND
Z1, Z2, Z3, Z6	10	2	5
Z4, Z5, Z7	10	2	
Z8, Z9	10		5

LAST SYMBOL	MISSING SYMBOL
C4 TP20 Z9	

NOTE:
 1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN; FOR COMPLETE DESIGNATION, PREFIX WITH UNIT NUMBER AND SUB-ASSEMBLY DESIGNATION(S) AS APPLICABLE.

PC396/A4625



2	RTIH-3	A4625
QTY / UNIT	MODEL USED ON	ASS'Y NO.
APPLICATION		
CODE		

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND INCLUDE CHEMICALLY APPLIED OR PLATED FINISHES

TOLERANCES ON
 DECIMALS .X ± .05
 .XX ± .01
 .XXX ± .005

FRACTIONS ± 1/64
 ANGLES ± 0° -30'

QTY. REQ.	ITEM	PART NO.	DESCRIPTION	SYMBOL
LIST OF MATERIAL				
FINAL APPROVAL: <i>[Signature]</i> DATE: 8-11-69 MECH. DES.: <i>[Signature]</i> DATE: 8-7-69 ELECT. DES.: <i>[Signature]</i> DATE: <i>[Signature]</i> CHECKED: <i>[Signature]</i> DATE: 6/19/69 DRAWN: <i>[Signature]</i> DATE:				
THE TECHNICAL MATERIEL CORP. MAMARONECK, NEW YORK				
DIAGRAM LOGIC MEMORY / GATING CIRCUIT (FOR DUAL 4 POSITION FUNCTIONS)				
SIZE	CODE IDENT.NO.	DWG NO.	ISSUE	
C	82679	CK 1611	Ø	
SCALE	NONE	SHEET	1	OF

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