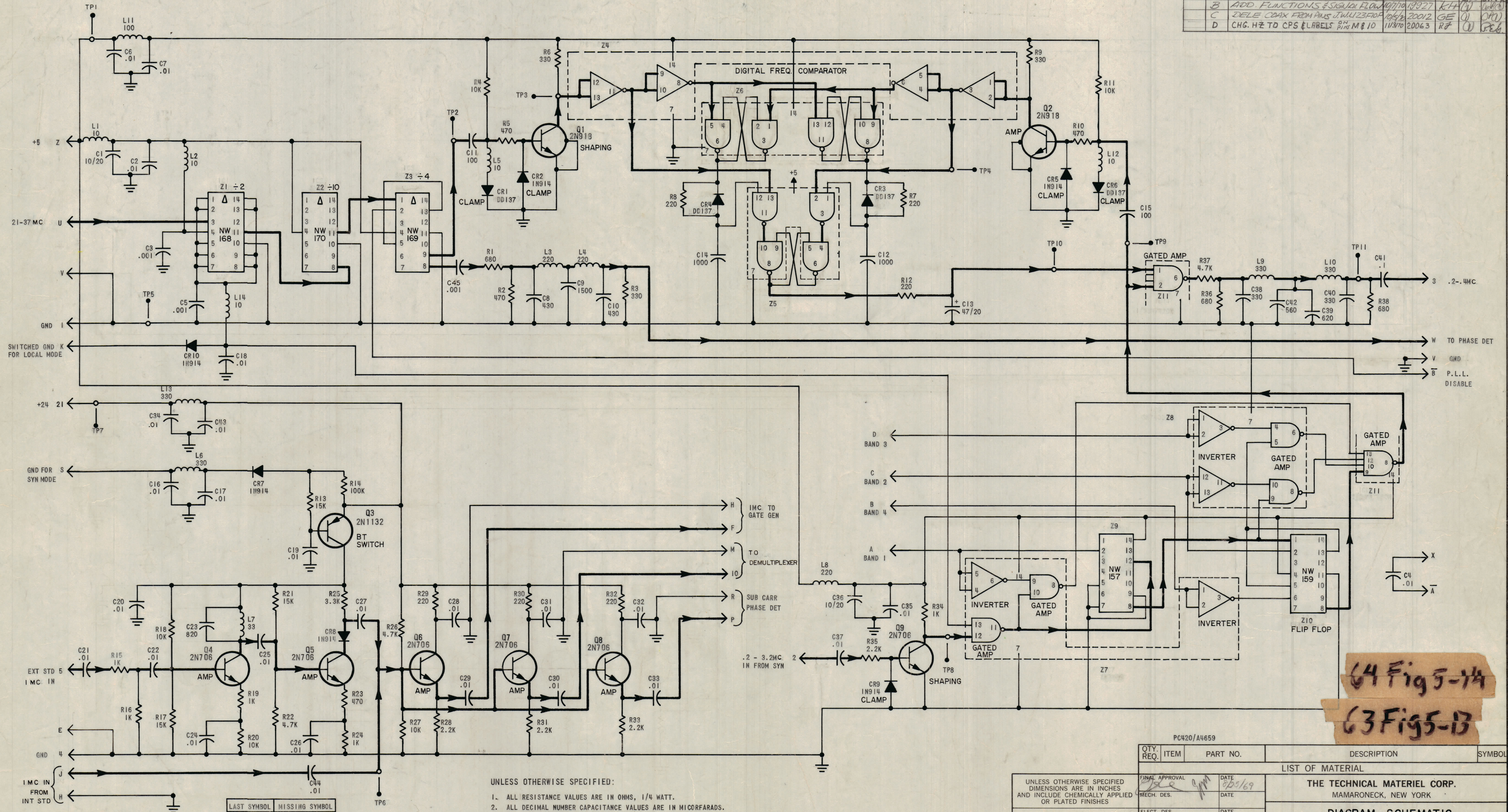


REVISIONS						
ZONE	LTR	DESCRIPTION	DATE	E.M.N.NO	DRAFT	CHKD APPD
X		EXP RELEASE	4/2/69	X		KD JLD
Ø		ORIGINAL RELEASE FOR PRODUCTION	4/2/69			()
A		REVISED SYMBOL CHART, Z11, C34, + PINT	9/24/69	19603	KD	KLARK
B		ADD FUNCTIONS & SIGNAL FLOW	10/7/70	19927	KH	() ()
C		DELE CDAX FROM PINS J, W, U, Z, F, D, P	10/5/72	20012	GE	() ()
D		CHG. HZ TO CPS & LABELS PINS M & IO	11/3/70	20063	RF	() ()



- UNLESS OTHERWISE SPECIFIED:
1. ALL RESISTANCE VALUES ARE IN OHMS, 1/4 WATT.
 2. ALL DECIMAL NUMBER CAPACITANCE VALUES ARE IN MICROFARADS. ALL WHOLE NUMBER CAPACITANCE VALUES ARE IN PICOFARADS.
 3. ALL INDUCTANCE VALUES ARE IN MICROHENRIES.
 4. ALL IC'S ARE NW176.

LAST SYMBOL	MISSING SYMBOL
C45	
CR10	
L14	
Q9	
R38	
TP11	
Z11	

HFRR-4		A4659	
QTY / UNIT	MODEL USED ON	ASS'Y NO.	
APPLICATION			
CODE			
MATERIAL			
FINISH			

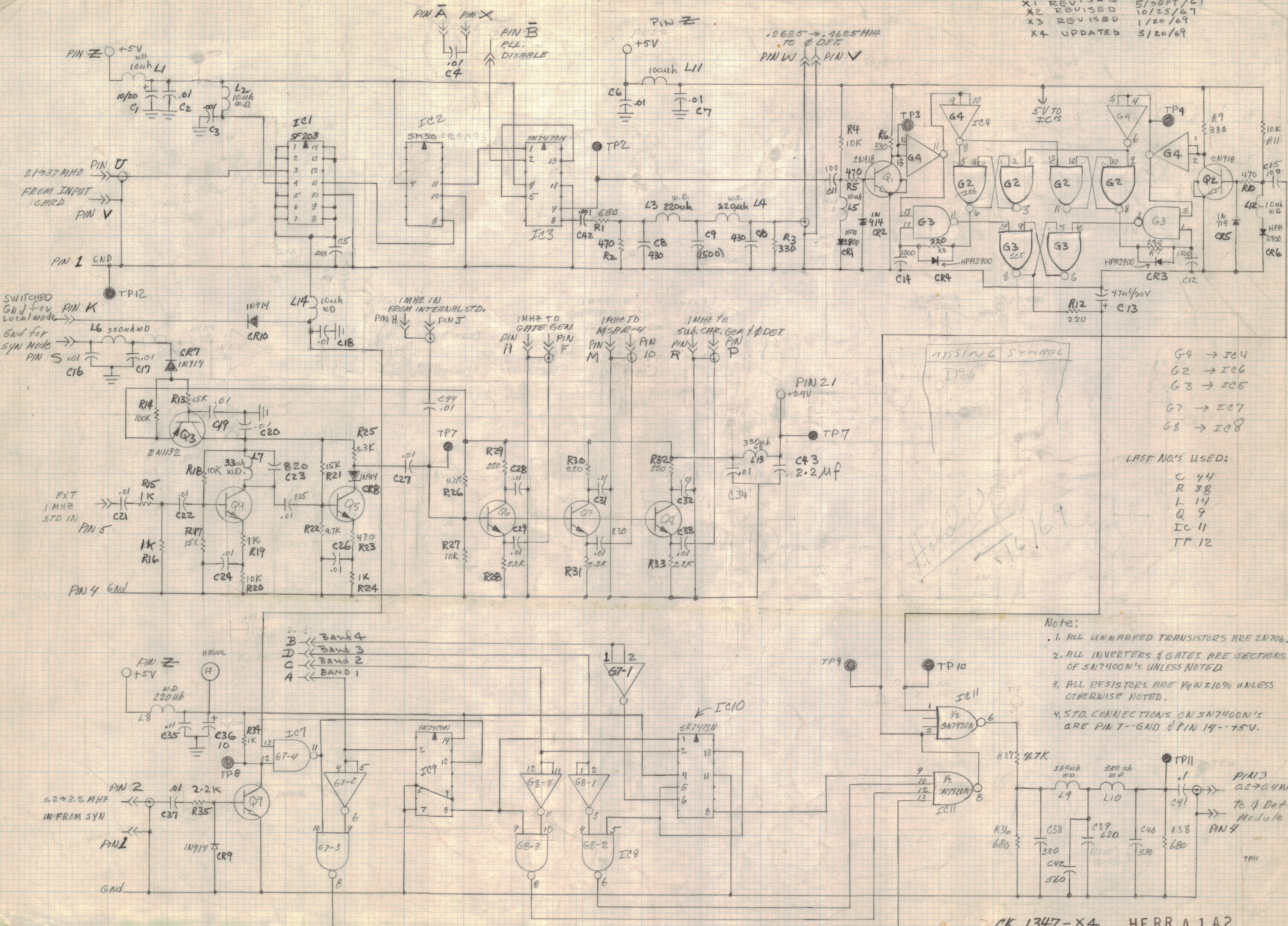
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND INCLUDE CHEMICALLY APPLIED OR PLATED FINISHES		DATE	
DECIMALS	FRACTIONS	DATE	
.X ± .05	± 1/64	DATE	
.XX ± .01	ANGLES	DATE	
.XXX ± .005	± 0° - 30'	DATE	

PC420/A4659			
QTY. REQ.	ITEM	PART NO.	DESCRIPTION
LIST OF MATERIAL			
THE TECHNICAL MATERIEL CORP.			
MAMARONECK, NEW YORK			
DIAGRAM SCHEMATIC			
PHASE DETECTOR DRIVER MODULE			
SIZE	CODE IDENT. NO.	DWG NO.	ISSUE
D	82679	CK 1347	D
SCALE NONE			SHEET OF

64 Fig 5-14
63 Fig 5-13

NOTICE TO PERSONS RECEIVING THIS DRAWING
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X1 REVISED 5/SEPT/67
 X2 REVISED 10/25/67
 X3 REVISED 1/20/69
 X4 UPDATED 5/20/69



G4 → IC4
 G2 → IC6
 G3 → IC5
 G7 → IC7
 G8 → IC8

LAST NO.'S USED:
 C 44
 R 38
 L 14
 Q 9
 IC 11
 TP 12

Note:
 1. ALL UNMARKED TRANSISTORS ARE 2N706.
 2. ALL INVERTERS & GATES ARE SECTIONS OF SN7400'S UNLESS NOTED.
 3. ALL RESISTORS ARE 1/4W ±10% UNLESS OTHERWISE NOTED.
 4. STD. CONNECTIONS ON SN7400'S ARE PIN 7--GND & PIN 14--+5V.

REV 1/20/69 REV: 10/25/67
 HFERRA1A2 WLB
 SEPT 5, 1967

XCK 1347-X4 HFERRA1A2
 PHASE DET. DRIVER MODULE A1A2
 P10 RECEIVER FREQUENCY READOUT ASSEMBLY
 325203A1A2-B
 W.L.B. 7/20/67