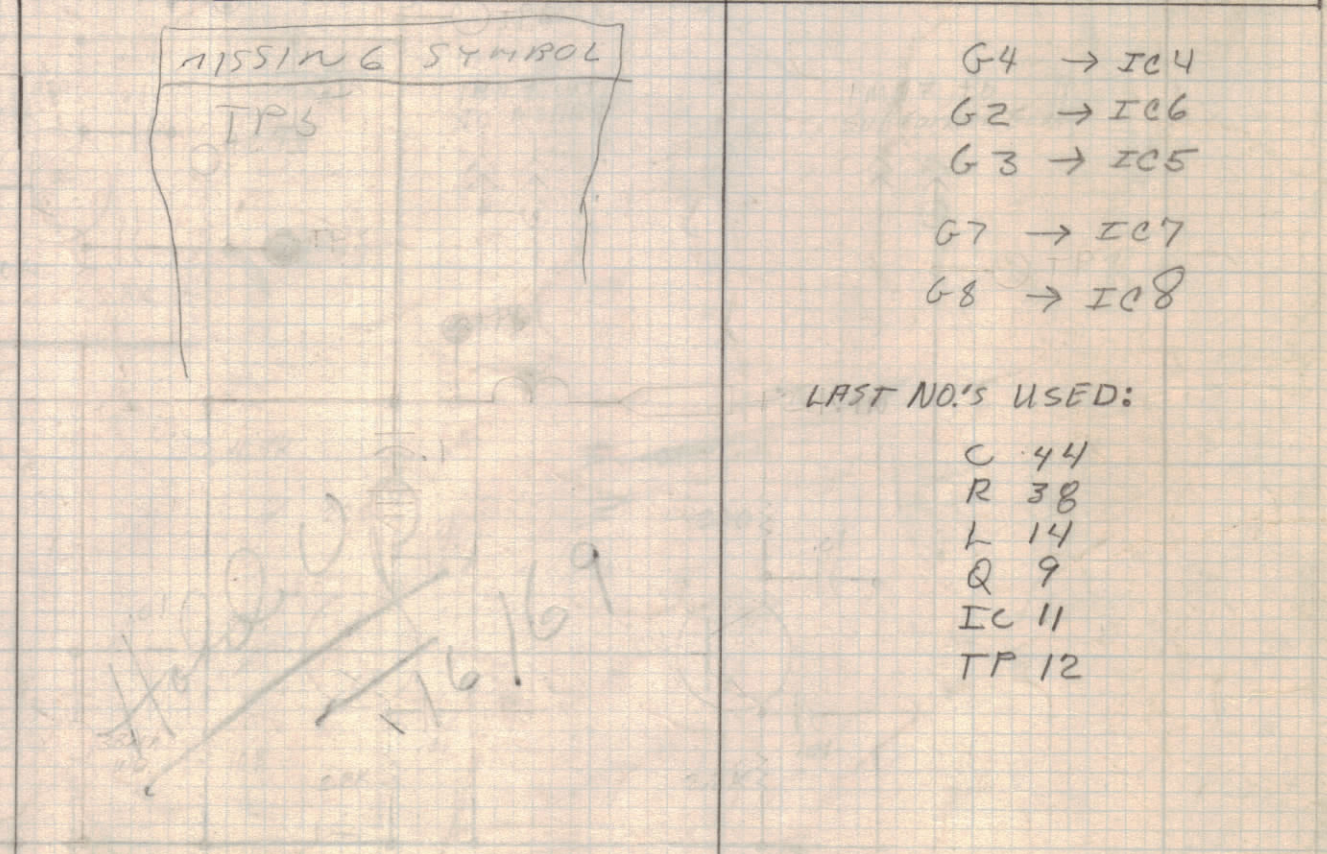
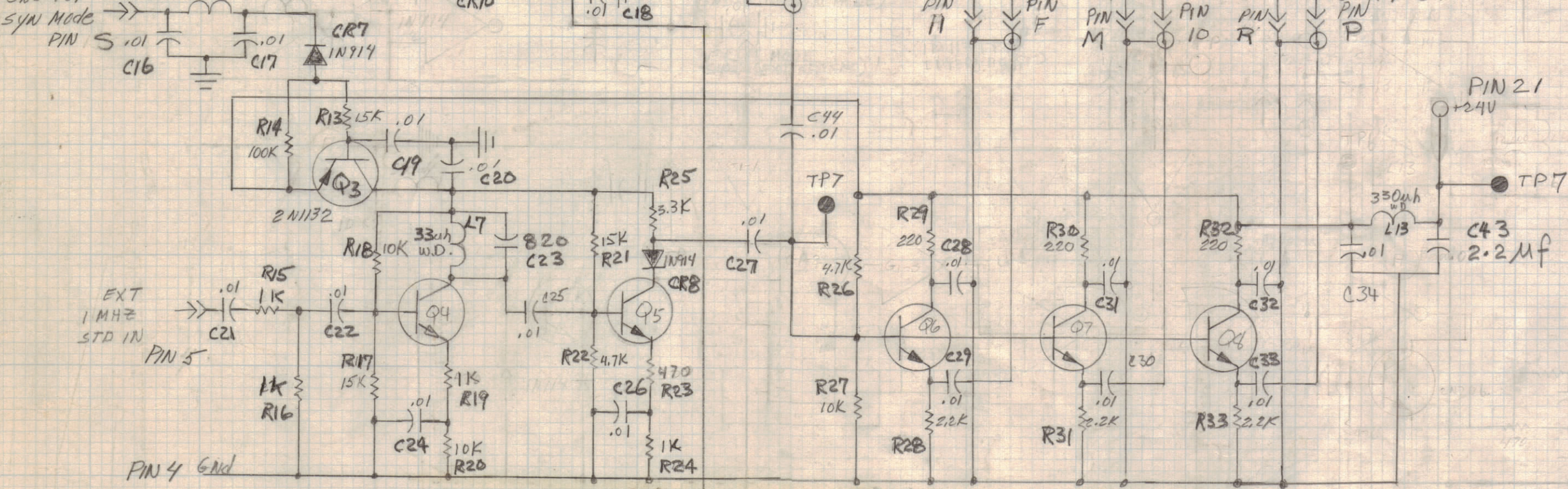
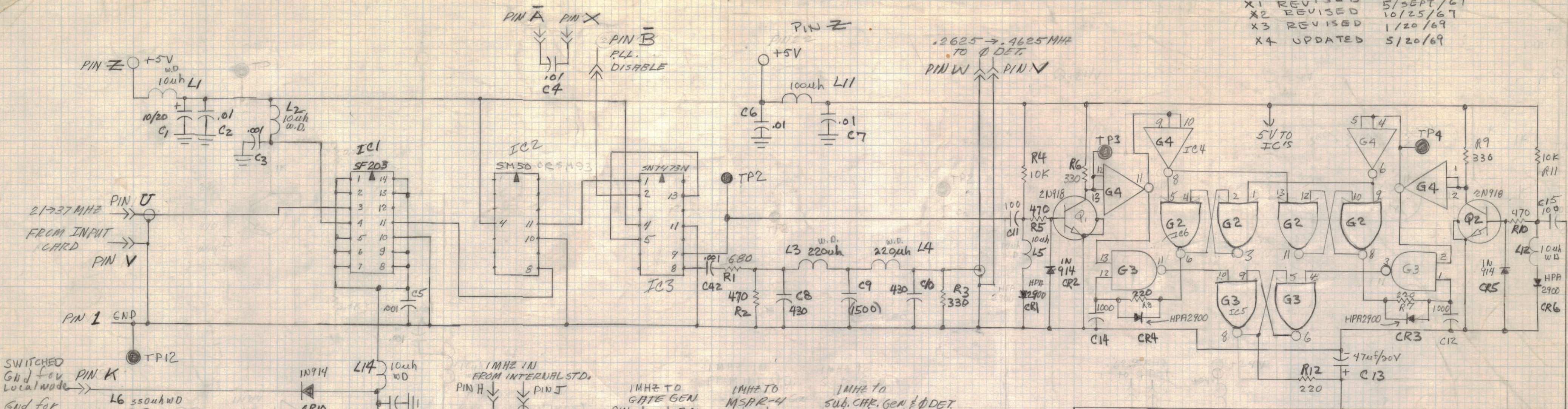


X1 REVISED 5/SEPT/67  
 X2 REVISED 10/25/67  
 X3 REVISED 1/20/69  
 X4 UPDATED 5/20/69



G4 → IC4  
 G2 → IC6  
 G3 → IC5  
 G7 → IC7  
 G8 → IC8

LAST NO.'S USED:  
 C 44  
 R 38  
 L 14  
 Q 9  
 IC 11  
 TP 12

- Note:
1. ALL UNMARKED TRANSISTORS ARE 2N706.
  2. ALL INVERTERS & GATES ARE SECTIONS OF SN7400'S UNLESS NOTED.
  3. ALL RESISTORS ARE 1/4W ±10% UNLESS OTHERWISE NOTED.
  4. STD. CONNECTIONS ON SN7400'S ARE PIN 7--GND & PIN 14--+5V.

REV 1/20/69 REV: 10/25/67  
 HFERRA1A2 WLB  
 SEPT 5, 1967

XCK 1347-X4 HFERRA1A2  
 PHASE DET. DRIVER MODULE A1A2  
 P10 RECEIVER FREQUENCY READOUT ASSEMBLY  
 325203A1A2-B  
 W.L.B. 7/20/67