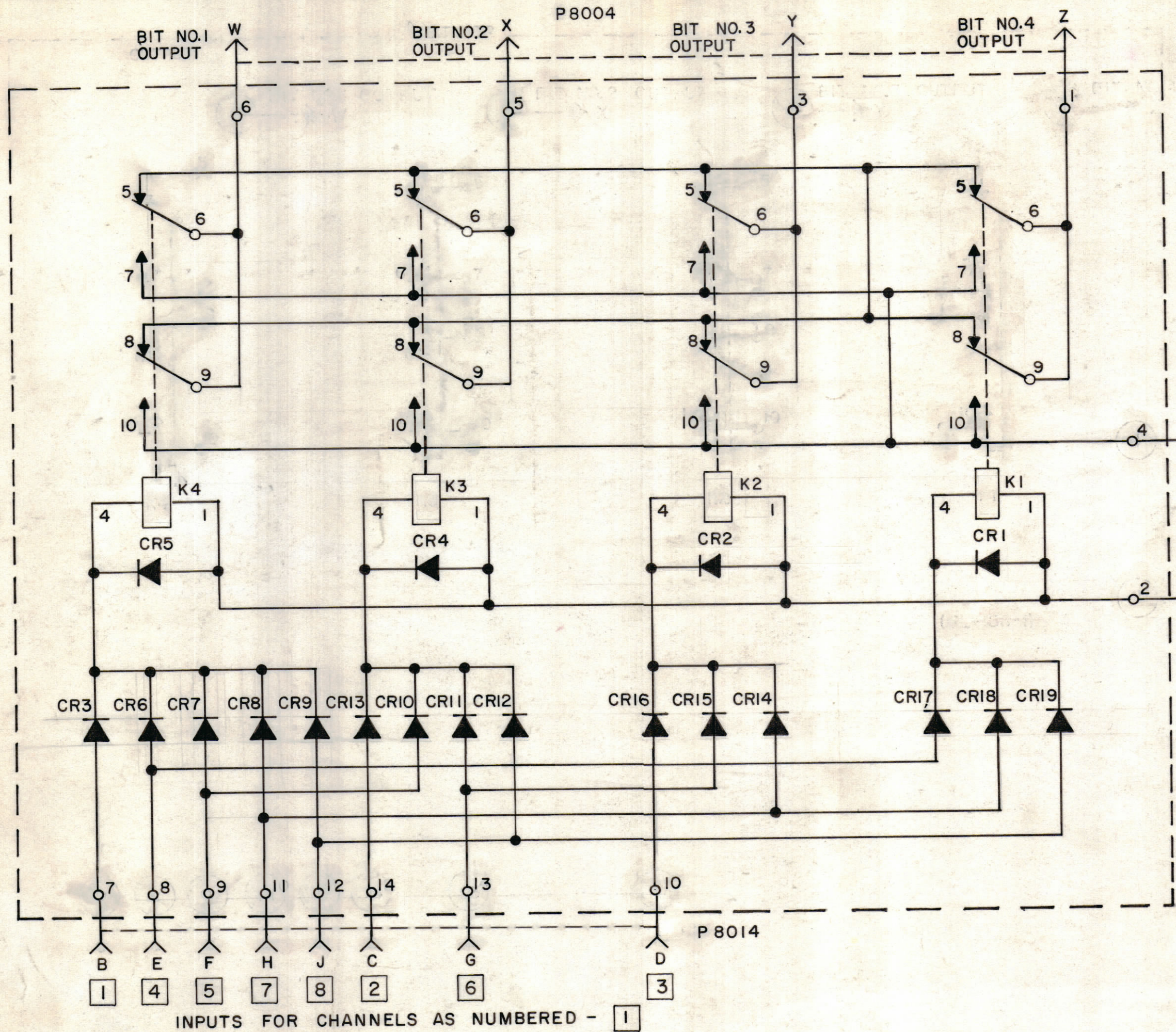
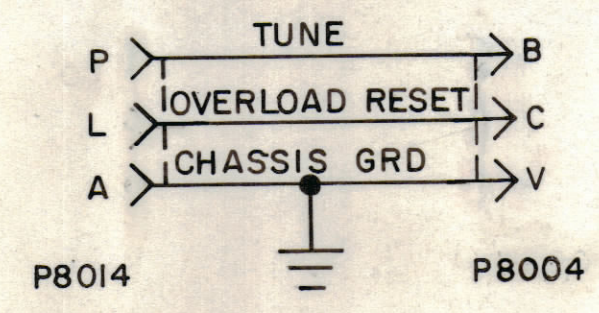


REVISIONS						
ZONE	LTR	DESCRIPTION	DATE	E.M.N.O	DRAFT	CHKD APPD
	X	EXPEPIMENTAL RELEASE	2-9-67	X	RME	
	Ø	ORIGINAL RELEASE FOR PRODUCTION	2-15-67	Ø	RME	
	A	P8004 WAS J8004. 3 PLACES. SCHEMATIC UPDATED.	4-3-67	18067	L.A.K.	PL2



- NOTES:
1. SHOWN WIRED-FOR NEG. VOLTAGE OPERATION - APPLY GROUND AT CHANNEL INPUT TERMS. (BIT OUTPUT IS NEG. VOLTAGE)
 2. FOR POS VOLTAGE OPERATION REMOVE JUMPER MARKED $\times \times$ GROUND PIN NO.2, APPLY POS VOLTAGE TO CHANNEL INPUT TERMS FROM PIN NO. "K" (BIT OUTPUT IS POS VOLTAGE)
 3. ALL DIODES IN4245 UNLESS OTHERWISE SPECIFIED

INPUTS FOR CHANNELS AS NUMBERED - [1]



LRCF - 1		
QTY / UNIT	MODEL USED ON	ASS'Y NO.
APPLICATION		
CODE		

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UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND INCLUDE CHEMICALLY APPLIED OR PLATED FINISHES

TOLERANCES ON
 DECIMALS .X ± .05
 .XX ± .01
 .XXX ± .005
 FRACTIONS ± 1/64
 ANGLES ± 0° -30'

MATERIAL
 FINISH

QTY. REQ.	ITEM	PART NO.	DESCRIPTION	SYMBOL
LIST OF MATERIAL				
THE TECHNICAL MATERIEL CORP. MAMARONECK, NEW YORK				
DIAGRAM, SCHEMATIC				
SIZE		CODE IDENT. NO.	DWG NO.	ISSUE
C		82679	CK 1232	A
SCALE: NONE			SHEET	OF