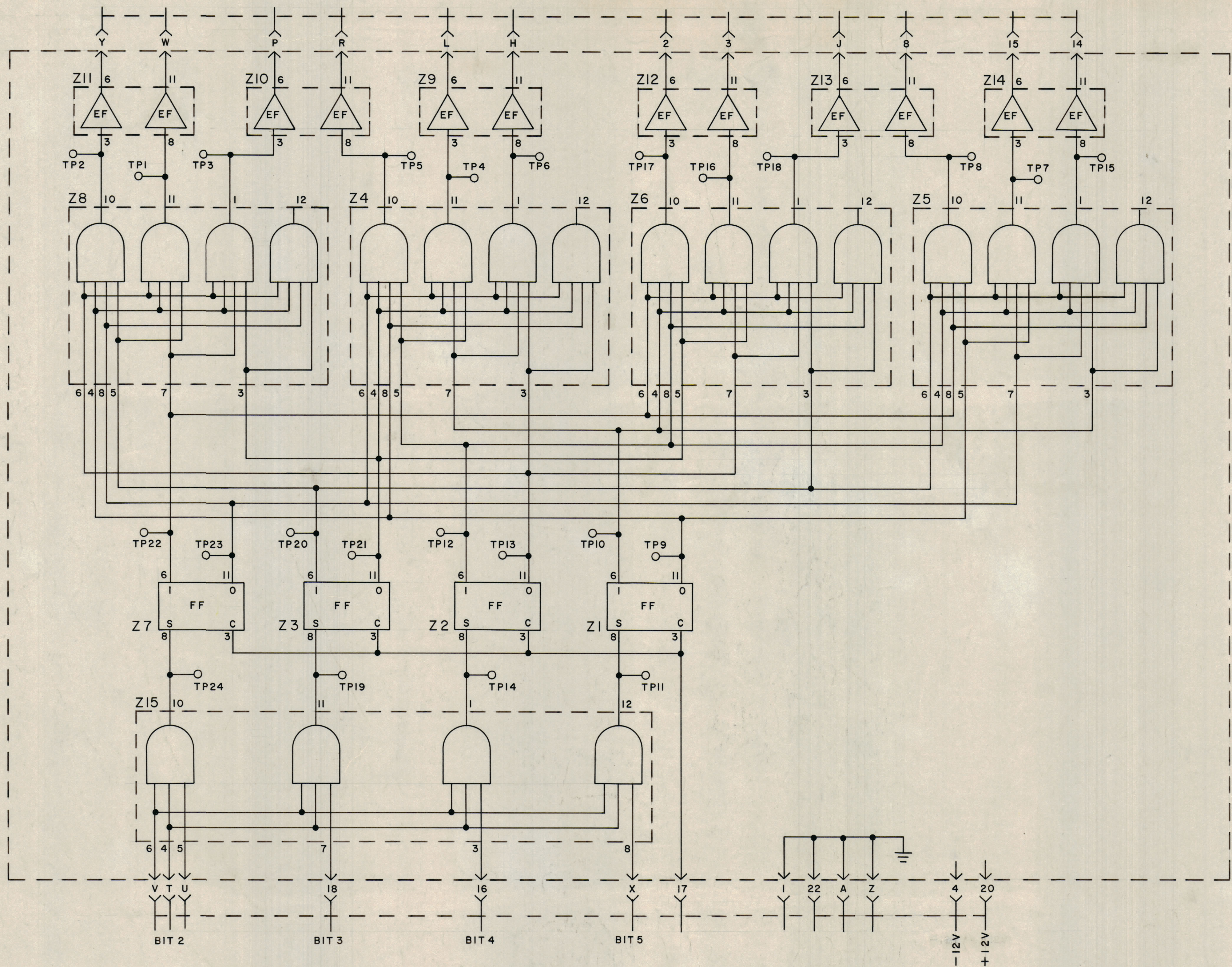


REVISIONS						
ZONE	SYM	DESCRIPTION	DATE	E.M.N. NO.	DRAFT	CHKD
2		ORIGINAL RELEASE FOR PRODUCTION	2/2/66			



MODULE VOLTAGE & GRD CHART

SYMBOL	PIN CONNECTION		
	+12V	-12V	GRD
Z1, 2, 3 & Z7	10	2	5
Z4 THRU Z6, 8 & Z15		2	
Z9 THRU Z14		2 & 7	5 & 12

- UNLESS OTHERWISE SPECIFIED -
 I - ON EACH FLIP FLOP (FF) PIN 6 IS THE "1" OUTPUT
 & PIN 11 IS THE "0" OUTPUT.

CK1148

REF: PC256

REQ'D	ITEM	PART NUMBER	DESCRIPTION	SYMBOL
EATON LIST OF MATERIAL				
MATERIAL				
THE TECHNICAL MATERIEL CORP. MAMARONECK, NEW YORK				
FINISH				
TITLE DIAGRAM, SCHEMATIC MEMORY/GATING CIRCUIT				
Q'TY./UNIT		MODEL USED ON		DATE
SCALE		CODE		DATE
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND INCLUDE CHEMICALLY APPLIED OR PLATED FINISHES				
THE CONTENTS OF THIS DRAWING ARE THE EXCLUSIVE PROPERTY OF THE TECHNICAL MATERIEL CORP. ITS UNAUTHORIZED USE OR REPRODUCTION IN WHOLE OR IN PART IS STRICTLY FORBIDDEN.		DECIMALS .X ± .05 .XX ± .01 .XXX ± .005	FRACTIONS ± 1/64 ANGLES ± 0° 30'	DATE
DRAWN J. LESHINSKI		DATE 7-7-66		FINAL APPROVAL DATE 7/20/66
CHECKED J. L. G. DL		DATE 7/22/66		DATE 7/20/66
ELECT. DES. DATE		MECH. DES. DATE		REV. LTR.
SHEET		SHEET		REV. LTR.

NOTES