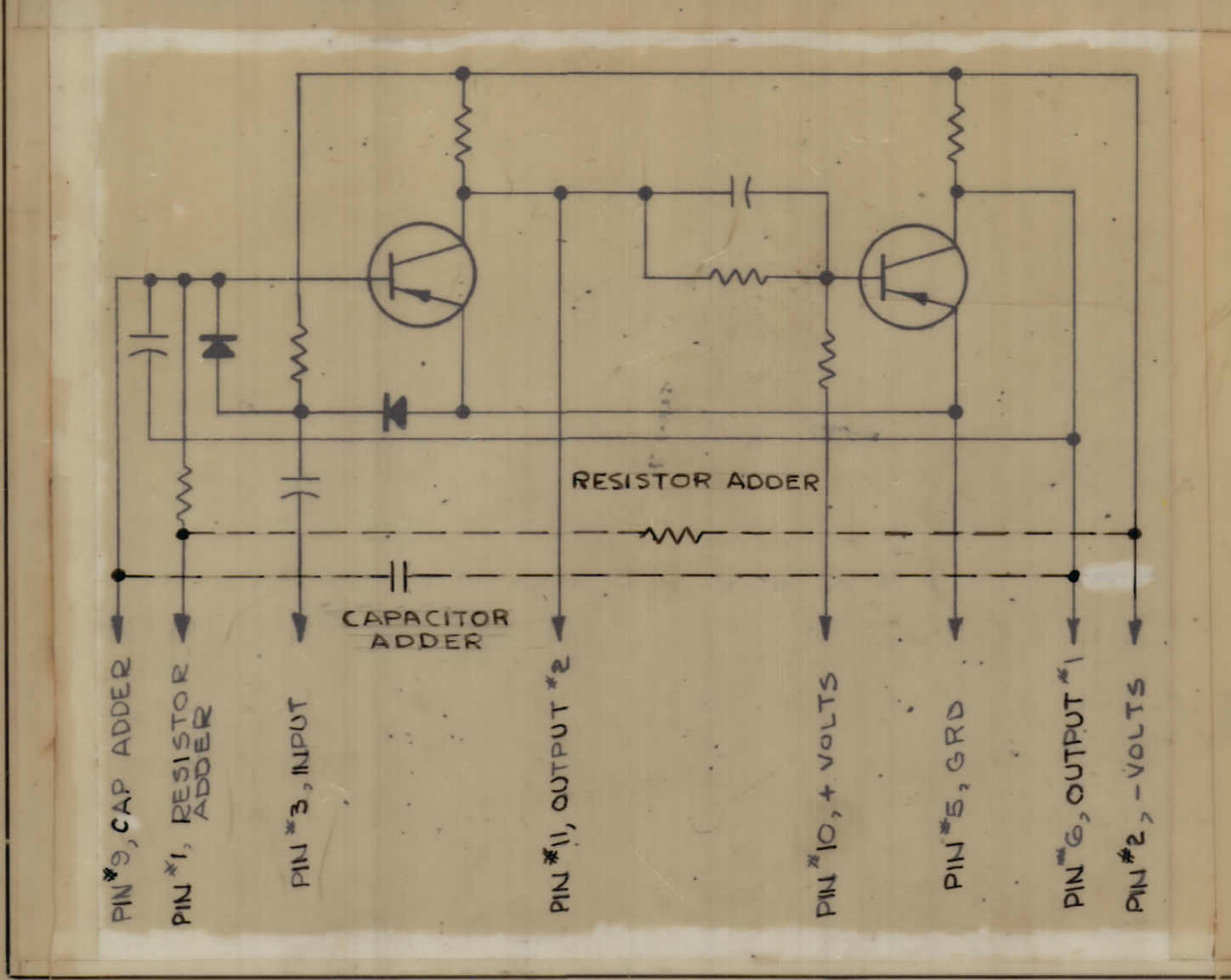


CARDS USED

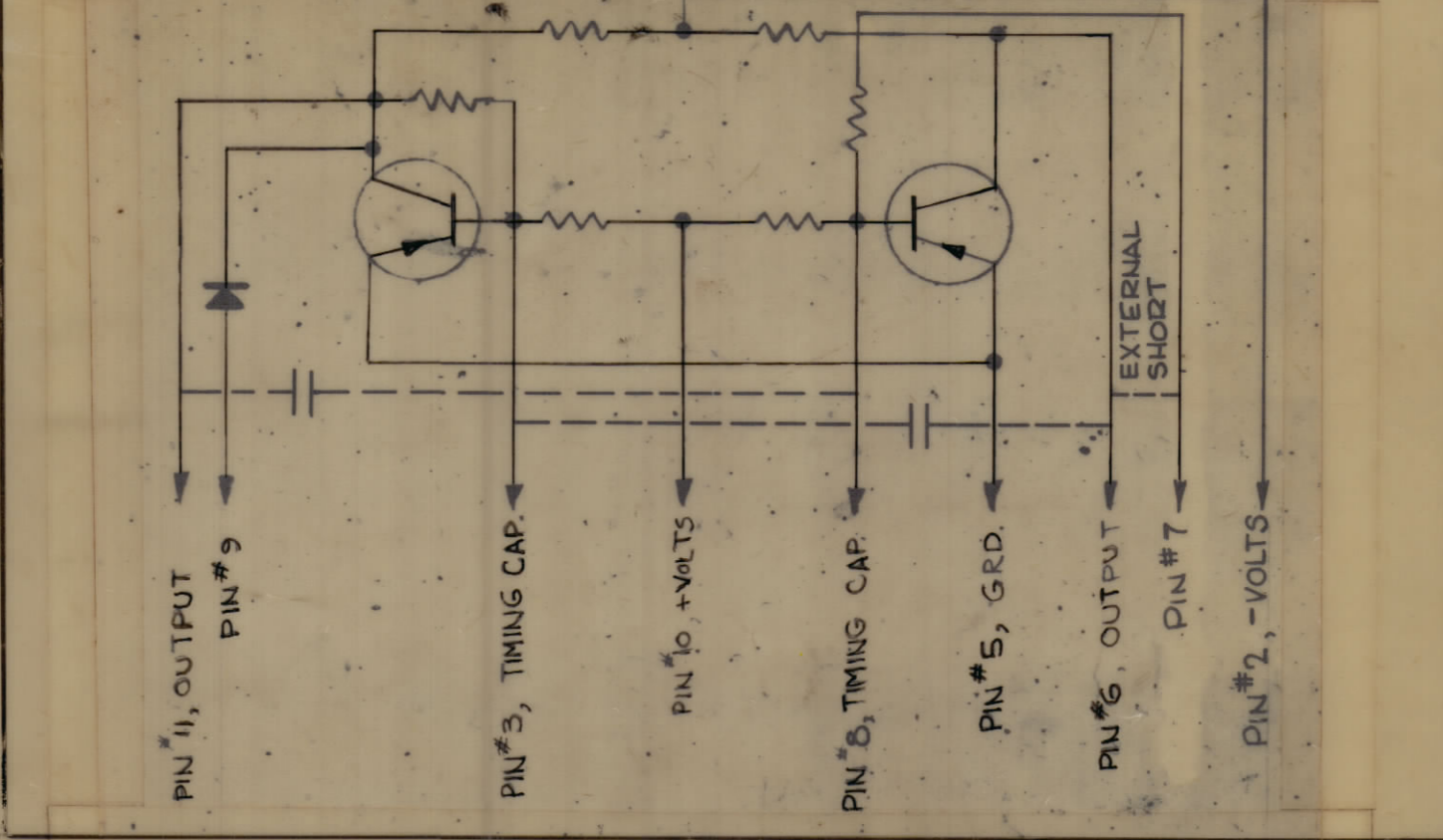
PART NO.	DESCRIPTION	CON. NO.	LOCATION	B+ INPUT	B- INPUT	GROUND
PC127/A3694	INITIAL RESET CIRCUIT	J5007	SHEET 2	PIN 5	PIN 37	1,2,4,3,44
PC220/A4295	SERIAL TO PARALLEL CLOCK CIRCUIT	J5008	2	5	37	1,2,4,3,44
PC221/A4294	SERIAL TO PARALLEL SHIFT REGISTER	J5009	2	5	37	1,2,4,3,44
PC218/A4254	STUNTING CARD, DOUBLE CHARACTER	J5010	3	5	37	1,2,4,3,44
PC169/A3812	ADVANCE, TUNE COMPLETE, BOY RELAY CKT.	J5011	1	5	37	1,2,4,3,44
PC175/A3823	ADVANCE PRIME TIMING CIRCUIT	J5012	2	5	37	1,2,4,3,44
PC171/A3820	MEMORY CORE INPUT SHIFT REGISTER	J5013	4	5	37	1,2,4,3,44
PC173/A3821	PRIME CIRCUIT CARD	J5014	3	5	37	1,2,4,3,44
PC176/A3824	ADVANCE 'O' & 'E' CIRCUIT	J5015	3	5	37	1,2,4,3,44
PC182/A3639	MEMORY CORE REGISTER	J5016	4	6	2	18
		J5017	4	6	2	18
		J5018	4	6	2	18
		J5019	4	6	2	18
PC182/A3639	MEMORY CORE REGISTER	J5020	4	6	2	18
PC158/A3800	DRIVE CIRCUIT FOR DECODER RELAYS	J5021	SHEET 4	5	37	1,2,4,3,44

REV.	DESCRIPTION	DATE	BY	CHKD	APPD
X1	"OS" AND "TG" TYPICAL COMPONENTS ADDED	10/16/65	WV	SM	
X2	TIMING CIRCUIT COMPONENT VALUES ADDED ON OS CIRCUIT CAPACITOR ADDBER WAS CONNECTED FROM PIN 9 & PIN 2 - ON OS-2 Z3 C2 WAS INT. SHUNT MODE - 2.2K FOR C2, NAG-B 2AG-B CLEARING	10-24-65	X2	GD	
A	ORIGINAL RELEASE	11-2-65		GD	
	SEE SHEET 2044 AND 4 OF 4	12/16/65		WV	



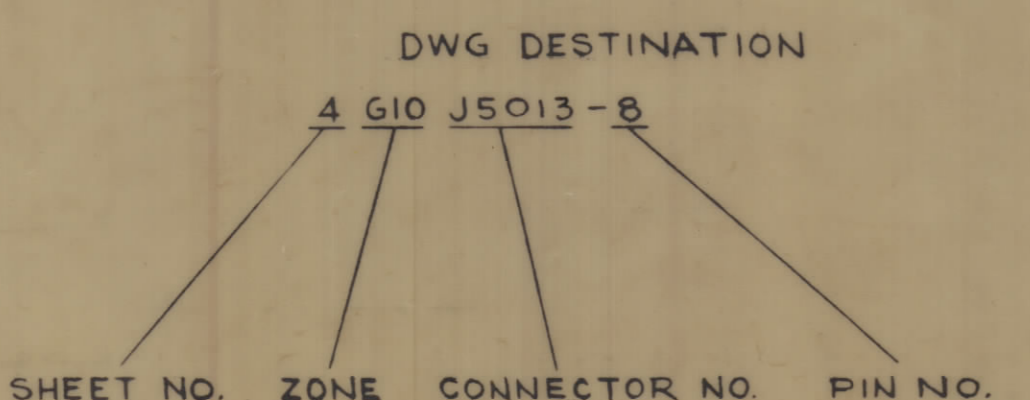
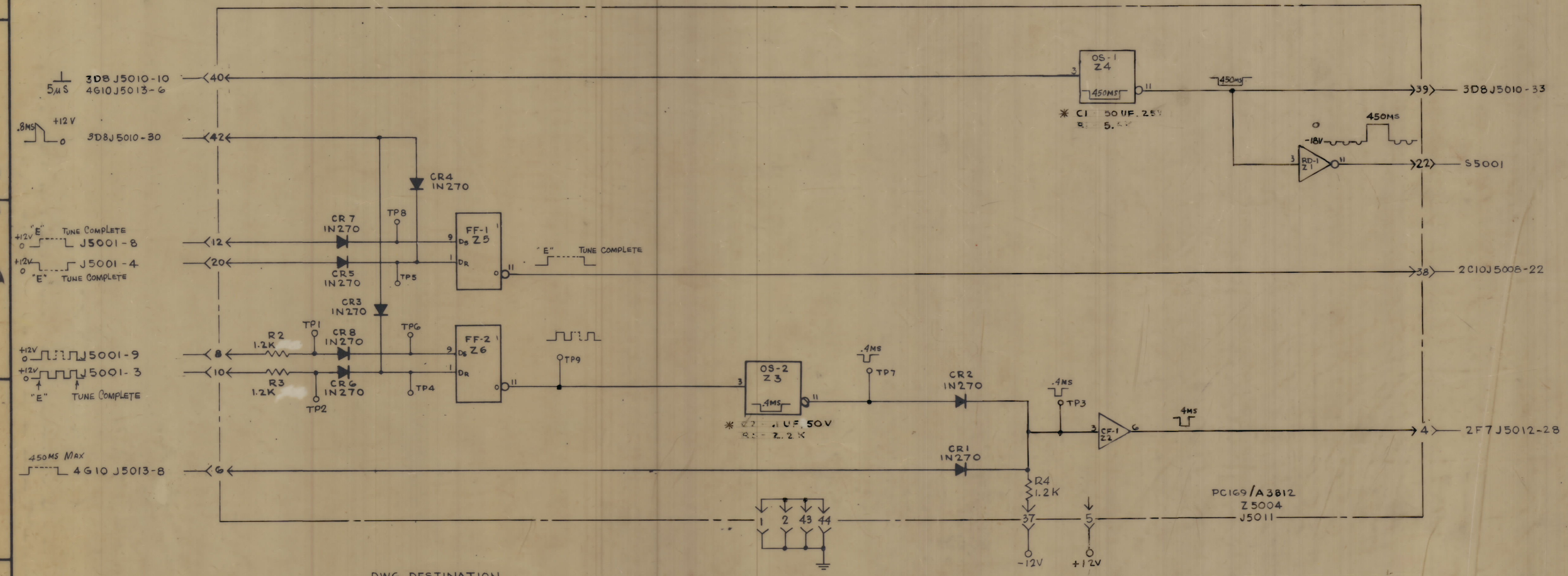
TYPICAL "OS" CIRCUIT

NOTE: \* INDICATES VALUES OF RESISTOR & CAPACITOR USED WHEN CALLED FOR BENEATH RESPECTIVE "OS" MODULES IN DIAGRAM.



TYPICAL "TG" CIRCUIT

NOTE: \*\* INDICATES VALUES OF CAPACITORS USED WHEN CALLED FOR BENEATH RESPECTIVE "TG" MODULE IN DIAGRAM. QTY OF CAPACITORS MAY VARY WHEN IT BECOMES NECESSARY TO PARALLEL TO ARRIVE AT DESIRED VALUE.



- UNLESS OTHERWISE SPECIFIED -  
I-ALL RESISTOR VALUES ARE IN OHMS, 1/2 WATT.

LAST NO. USED	MODULE DESIGN CODE
FF-23	FLIP FLOP
OS-16	ONE SHOT
CF-5	COMPLEMENTARY EMITTER FOLLOWER
RD-2	RELAY DRIVER
IV-2	INVERTER
PF-10	POSITIVE EMITTER FOLLOWER
NAG-8	NAND GATE
NA-11	NON INVERTING AMPLIFIER
NG-2	NOR GATE
TG-2	TIMING GENERATOR
AG-8	AND GATE

REQD	ITEM	PART NUMBER	DESCRIPTION	SYMBOL
EATON LIST OF MATERIAL				
THE TECHNICAL MATERIEL CORP. MAMARONECK, NEW YORK				
TITLE: DIAGRAM LOGIC DETAILED RTMU-2				
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND INCLUDE CHEMICALLY APPLIED OR PLATED FINISHES				
DATE	10/16/65	DATE	11/16/65	DATE
CHECKED	WV	DATE	10/29/65	DATE
DESIGNED	WV	DATE		DATE
FRACIONS	2/164	ANGLES	2 0' 30"	
TOLERANCES				
M/L 7/21/65				