

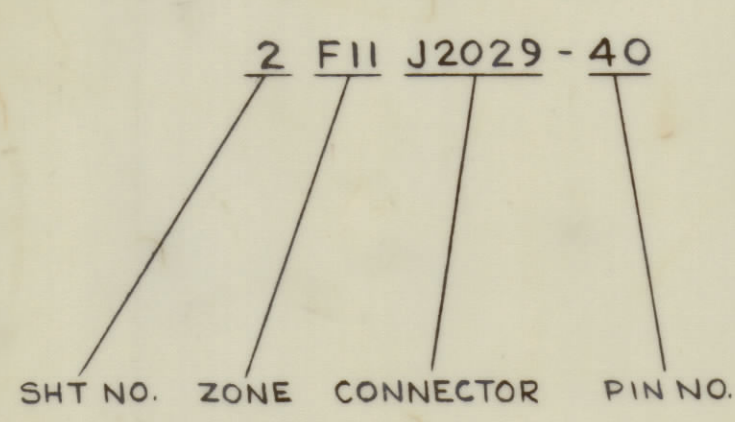
REVISIONS						
BYM	DESCRIPTION	DATE	EM N NO	DRAFT	CHKD	APPD
	ORIGINAL RELEASE	7/4/65	H	E		
A	COMPLETELY REVISED	11/16/65	15538			

CARDS USED

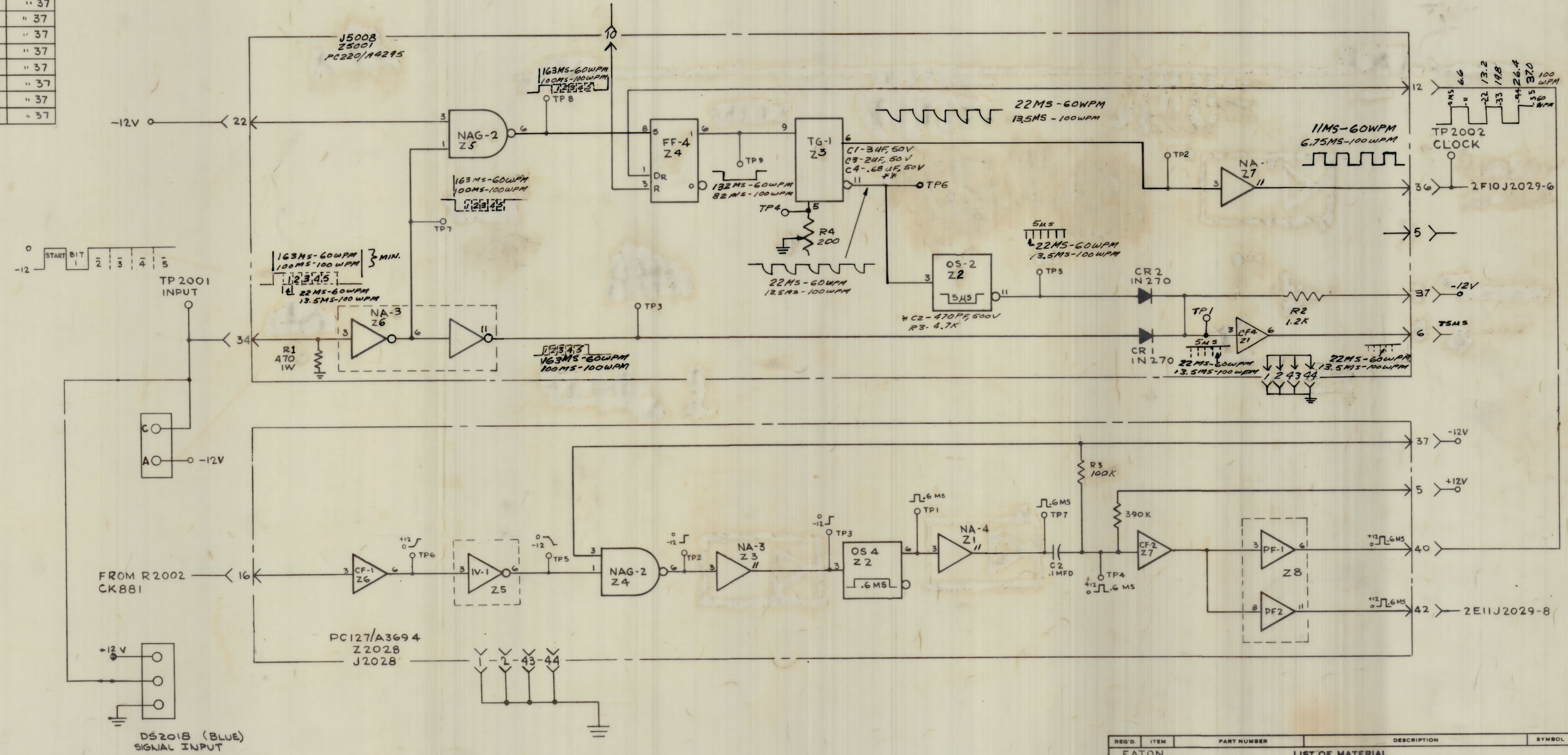
PART NO.	DESCRIPTION	CONNECTOR NO.	LOCATION	B+ INPUT	B- INPUT
PC144/A3761	RESET CKT FOR MEMORY FF'S	J2001	SHT 3	PIN 5	PIN 37
PC141/A3758	SHIFT REGISTER FOR MEMORY FF'S	J2002	SHT 6	" 5	" 37
PC121/A3688	READOUT MEMORY FF'S	J2003	SHT 6	" 5	" 37
"	"	J2004	" 6	" 5	" 37
PC124/A3691	READOUT CKT FOR KC NIXIE DRIVERS	J2005	" 6	"	" 37
PC141/A3758	SHIFT REGISTER FOR MEMORY FF'S	J2009	" 7	" 5	" 37
PC121/A3688	SHIFT REGISTER MEMORY FF'S	J2012	" 7	" 5	" 37
PC123/A3690	READOUT DRIVE CKT	J2013	" 7	"	" 39
PC124/A3691	R'OUT DRIVE CKT FOR NIXIE DRIVERS	J2016	" 5	"	" 37
PC121/A3688	READOUT MEMORY FF'S	J2017	" 5	" 5	" 37
"	"	J2018	" 5	" 5	" 37
PC138/A3755	AND-OR LOGIC FOR MC R'OUTS	J2019	" 4	" 5	" 37
PC145/A3754	NOR LOGIC FOR MC I0'S, 20'S, 30'S	J2020	" 4	" 5	" 37
PC140/A3757	NOR LOGIC FOR MC I'S	J2021	" 4	" 5	" 37
"	"	J2022	" 4	" 5	" 37
PC143/A3760	FIRST LOGIC STAGE-MC R'OUTS	J2023	" 3	"	" 37
PC121/A3688	READOUT MEMORY FF'S	J2024	" 3	" 5	" 37
PC142/A3759	MC SHIFT REGISTER	J2025	" 3	" 5	" 37
PC160/A3794	TIMING & RESET CKT	J2026	" 2	" 5	" 37
PC139/A3756	CODE STORAGE - INPUT SH. REG.	J2027	" 2	" 5	" 37
PC127/A3694	INITIAL RESET CKT	J2028	" 1	" 5	" 37
PC221/A 4294	SERIAL TO PARALLEL SH. REG.	J2029	" 2	" 5	" 37
PC220/A4295	SER. TO PAR. CLOCK CKT	J2030	" 1	" 5	" 37

NOTES:  
 1 - GROUND CONNECTION THRU PINS 1, 2, 43 & 44 FOR ALL CARDS.

DWG DESTINATION



LAST NUMBER USED	
CF2	
FF1	
IV1	
NA4	
NAG2	
OS4	
PF2	
TG1	



REQ'D	ITEM	PART NUMBER	DESCRIPTION	SYMBOL
EATON LIST OF MATERIAL				
MATERIAL THE TECHNICAL MATERIEL CORP. MAMARONECK, NEW YORK				
FINISH TITLE DIAGRAM LOGIC DETAILED (RT1E-1)				
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND INCLUDE CHEMICALLY APPLIED OR PLATED FINISHES				
DECIMALS	1/2 = .50	FRACTIONS	3/16	DATE
TOLERANCES	± .01	MECH. DIM.	± .005	DATE