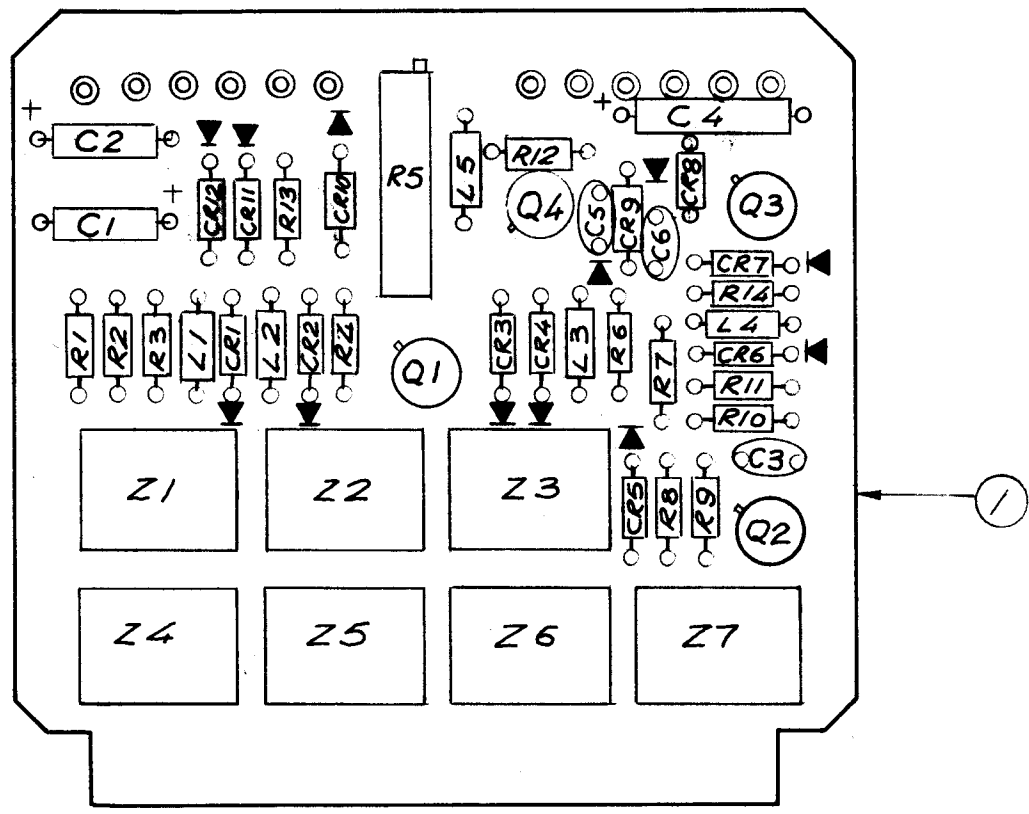


REVISIONS						
ZONE	LTR	DESCRIPTION	DATE	E.M.N.NO	DRAFT	CHKD APPD
	X	EXP RELEASE	7/17/69		GE	
	Ø	ORIGINAL RELEASE	8/18/69		Q	



ASSEMBLY NOTES

1. TO MOUNT COMPONENTS, INSERT LEADS THROUGH PLATED THRU-HOLES.
2. CAUTION, WHEN APPLYING HEAT AND SOLDER TO LEAD AND FOIL.
3. CLEAN AND INSPECT AS PER SPEC. S 676.
4. FOR ELECTRICAL COMPONENT PART NUMBERS REFER TO NPL-A 4572.
5. USE SYMBOL NUMBERS FOR ASSEMBLY REFERENCE.

REQ'D	ITEM	PART NUMBER	DESCRIPTION	SYM.
	4	3	PX 829-3	PAD MOUNTING TRANSISTOR
	X	2	BS 100	SOLDER TIN ALLOY
	1	1	PC 364	PRINTED CIRCUIT BOARD

1	RTTD-5A	
1	RTTD-5	
1	RTTD-4	
QTY / UNIT	MODEL USED ON	ASS'Y NO.

APPLICATION

CODE

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UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND INCLUDE CHEMICALLY APPLIED OR PLATED FINISHES

DECIMALS: .X ± .05, .XX ± .01, .XXX ± .005
 FRACTIONS: 1/64, ANGLES: 0°-30'

MATERIAL

FINISH

LIST OF MATERIAL

THE TECHNICAL MATERIEL CORP.
MAMARONECK, NEW YORK

ASSY. PRINTED CIRCUIT BD.
LEDEX GATING

FINAL APPROVAL <i>S. De Marco</i>	DATE 8-18-69
MECH. DES. <i>R. Uzzo</i>	DATE 8-4-69
ELECT. DES.	DATE
CHECKED	DATE
DRAWN <i>G. EVANGELIST</i>	DATE 7/17/69

SIZE B	CODE IDENT. NO. 82679	DWG NO. A 4572	ISSUE Ø
SCALE		SHEET OF	